

**SPL (Simple Programming Language)  
Syntax**

**Basic Statements**

- **skip**
- assignment  
$$\underbrace{(u_1, \dots, u_k)}_{\text{variables}} := \underbrace{(e_1, \dots, e_k)}_{\text{expressions}}$$
- **await**  $c$   
(where  $c$  is a boolean expression)  
special case: **halt**  $\equiv$  **await**  $F$
- Communication by message-passing  
 $\alpha \leftarrow e$  (send)  
 $\alpha \Rightarrow u$  (receive)  
(where  $\alpha$  is a channel)
- Semaphore operations  
**request**  $r$  ( $r > 0 \rightarrow r := r - 1$ )  
**release**  $r$  ( $r := r + 1$ )  
(where  $r$  is an integer variable)

## SPL (CON'T)

No program variables are modified by schematic statements. One exception:

“ $x$ ” in **produce**  $x$

### Schematic Statements

In Mutual-Exclusion programs:

- **noncritical**  
may not terminate
- **critical**  
terminates

In Producer-Consumer programs:

- **produce**  $x$   
terminates – assign nonzero value to  $x$
- **consume**  $y$   
terminates

## SPL (CON'T)

### Compound Statements

- Conditional  
if  $c$  then  $S_1$  else  $S_2$   
if  $c$  then  $S$

- Concatenation  
 $S_1; \dots; S_k$

Example:

when  $c$  do  $S \equiv$  await  $c; S$

- Selection  
 $S_1$  or  $\dots$  or  $S_k$

- while  
while  $c$  do  $S$

Example:

loop forever do  $S \equiv$  while T do  $S$

## SPL (CON'T)

### Compound Statements (Con't)

- Cooperation Statement

$l: \underbrace{[\ell_1: S_1; \widehat{\ell}_1: ]}_{\text{process}} \parallel \dots \parallel [\ell_k: S_k; \widehat{\ell}_k: ]; \widehat{\ell}:$

$S_1, \dots, S_k$  are parallel to one another  
interleaved execution.

entry step: from  $l$  to  $\ell_1, \ell_2, \dots, \ell_k$ ,

exit step: from  $\widehat{\ell}_1, \widehat{\ell}_2, \dots, \widehat{\ell}_k$  to  $\widehat{\ell}$ .

- Block

[ local declaration;  $S$  ]

local  $variable, \dots, variable : type$  where  $\underbrace{\varphi_i}$

$y_1 = e_1, \dots, y_n = e_n$

## SPL (CON'T)

Basic types – boolean, integer, character, ...

Structured types – array, list, set, ...

Static variable initialization

(variables get initialized at the start of the execution)

## Programs

$$P :: [ \textit{declaration}; P_1 :: [ \ell_1 : S_1; \hat{\ell}_1 : ] \parallel \dots \parallel P_k :: [ \ell_k : S_k; \hat{\ell}_k : ] ]$$

$P_1, \dots, P_k$  are top-level processes

Variables in  $P$  called program variables

### Declaration

$$\textit{mode} \underbrace{\textit{variable}, \dots, \textit{variable}}_{\text{program variables}} : \textit{type} \textbf{where} \varphi_i$$

↓

↓

**in** (not modified)

constraints on

**local**

initial values

**out**

$\varphi_1 \wedge \dots \wedge \varphi_n$  data-precondition of the program

## Channel Declaration

- synchronous channels  
(no buffering capacity)

*mode*  $\alpha_1, \alpha_2, \dots, \alpha_n$ : **channel of type**

- asynchronous channels  
(unbounded buffering capacity)

*mode*  $\alpha_1, \alpha_2, \dots, \alpha_n$ : **channel [1..] of type**  
**where**  $\varphi_i$

- $\varphi_i$  is optional
- $\varphi_i = \Lambda$  (empty list) by default

## Labels

$\ell : S$

- Label  $\ell$  identifies statement  $S$
- Equivalence Relation  $\sim_L$  between labels:
  - For  $\ell$ :  $[\ell_1 : S_1; \dots; \ell_k : S_k]$   
 $\ell \sim_L \ell_1$
  - For  $\ell$ :  $[\ell_1 : S_1 \text{ or } \dots \text{ or } \ell_k : S_k]$   
 $\ell \sim_L \ell_1 \sim_L \dots \sim_L \ell_k$
  - For  $\ell$ : **[local declaration;  $\ell_1 : S_1$ ]**  
 $\ell \sim_L \ell_1$



## Locations

[ $\ell$ ]

Identify site of control

- [ $\ell$ ] is the location corresponding to label  $\ell$ .
- Multiple labels identifying different statements may identify the same location.

$$[\ell] = \{\ell' \mid \ell' \sim_L \ell\}$$

**Example:** Fig 0.1: A fully labeled program

$$\begin{array}{ll} [\ell_0] = [\ell_1] = \{\ell_0, \ell_1\} & [\ell_6] = \{\ell_6\} \\ [\ell_2] = \{\ell_2, \ell_3, \ell_5\} & [\ell_7] = \{\ell_7\} \\ [\ell_4] = \{\ell_4\} & [\ell_8] = \{\ell_8\} \end{array}$$

**Example:** Fig 0.2: A partially labeled program

$$\begin{array}{l} \ell_0 \\ \ell_3 \rightarrow \ell_2^a \\ \ell_5 \rightarrow \ell_2^b \end{array}$$

**shortcut:** label  $\ell_2$  “represents”  $\{\ell_2, \ell_2^a, \ell_2^b\}$

```
in   a, b : integer where a > 0, b > 0
local y1, y2: integer where y1 = a, y2 = b
out  g     : integer
```

```

 $\ell_1$ : while y1  $\neq$  y2 do
     $\ell_2$ :  $\left[ \begin{array}{l} \ell_2^a$ : await y1 > y2;  $\ell_4$ : y1 := y1 - y2 \\ or \\ \ell_2^b: await y2 > y1;  $\ell_6$ : y2 := y2 - y1 \end{array} \right]
 $\ell_7$ : g := y1
 $\ell_8$ :
```

Figure 0.2

A Partially Labeled Program GCD

## Post Location

$$\ell: S; \widehat{\ell}: \quad post(S) = [\widehat{\ell}]$$

- For  $[\ell_1: S_1; \widehat{\ell}_1: ] \parallel \dots \parallel [\ell_k: S_k; \widehat{\ell}_k: ]$   
 $post(S_i) = [\widehat{\ell}_i]$ , for every  $i = 1, \dots, k$
- For  $S = [\ell_1: S_1; \dots; \ell_k: S_k]$   
 $post(S_i) = [\ell_{i+1}]$ , for  $i = 1, \dots, k-1$   
 $post(S_k) = post(S)$
- For  $S = [\ell_1: S_1 \text{ or } \dots \text{ or } \ell_k: S_k]$   
 $post(S_1) = \dots = post(S_k) = post(S)$
- For  $S = [\text{if } c \text{ then } S_1 \text{ else } S_2]$   
 $post(S_1) = post(S_2) = post(S)$
- For  $[\ell : \text{while } c \text{ do } S']$   
 $post(S') = [\ell]$

Example: Post Locations of Fig 0.2

$$post(\ell_1) = [\ell_7]$$

$$post(\ell_2) = post(\ell_4) \\ = post(\ell_6) = [\ell_1]$$

$$post(\ell_2^a) = [\ell_4]$$

$$post(\ell_2^b) = [\ell_6]$$

$$post(\ell_7) = [\ell_8]$$



## Ancestor

$S$  is an ancestor of  $S'$   
if  $S'$  is a substatement of  $S$

$S$  is a common ancestor of  $S_1$  and  $S_2$   
if it is an ancestor of both  $S_1$  and  $S_2$

$S$  is a least common ancestor (LCA) of  $S_1$  and  $S_2$   
if  $S$  is a common ancestor of  $S_1$  and  $S_2$   
and any other common ancestor  
of  $S_1$  and  $S_2$  is an ancestor of  $S$

LCA is unique for given statements  $S_1$  and  $S_2$

**Example:**  $[S_1; [S_2||S_3]; S_4] || S_5$

LCA of $S_2, S_3$	$[S_2  S_3]$
LCA of $S_2, S_4$	$[S_1; [S_2  S_3]; S_4]$
LCA of $S_2, S_5$	$[S_1; [S_2  S_3]; S_4]    S_5$

## Parallel Labels

- Statements  $S$  and  $\tilde{S}$  are parallel if  
their LCA is a cooperation statement  
that is different from statements  $S$  and  $\tilde{S}$

**Example:**  $S = [S_1; [S_2||S_3]; S_4] || S_5$

<u>Statements</u>	<u>LCA</u>
$S_2$ parallel to $S_3$	$S_2    S_3$
$S_2$ parallel to $S_5$	$S$
$S_2$ not parallel to $S_4$	$[S_1; \dots; S_4]$ not coop.
$S_2$ not parallel to $S_2    S_3$	$S_2    S_3$ same

- parallel labels – labels of parallel statements

## Conflicting Labels

conflicting labels – not equivalent and not parallel

Example:

$$\left[ \begin{array}{l} l_1: S_1; \\ l_2: ([l_3: S_3; \hat{l}_3:] \parallel [l_4: S_4; \hat{l}_4:]); \\ l_5: S_5; \hat{l}_5: \end{array} \right] \parallel [l_6: S_6; \hat{l}_6:]$$

$l_3$  is parallel to each of  $\{l_4, \hat{l}_4, l_6, \hat{l}_6\}$   
and in conflict with each of  
 $\{l_1, l_2, \hat{l}_3, l_5, \hat{l}_5\}$

$l_6$  and  $\hat{l}_6$  are in conflict with each other  
but are parallel to each of  
 $\{l_1, l_2, l_3, \hat{l}_3, l_4, \hat{l}_4, l_5, \hat{l}_5\}$

## Critical References

Writing References:

$$\begin{array}{cccc} x := \dots & \alpha \Rightarrow u & \text{produce } x & \text{request } r \\ \uparrow & & \uparrow & \uparrow \\ & & & \text{release } r \\ & & & \uparrow \end{array}$$

Reading References: all other references

critical reference of a variable in  $S$  if:

- writing ref to a variable that has reading or writing refs in  $S'$  (parallel to  $S$ )
- reading reference to a variable that has writing references in  $S'$  (parallel to  $S$ )
- reference to a channel

## Limited Critical References (LCR)

Statement obeys LCR restriction (LCR-Statement)  
 if each test (for await, conditional, while)  
 and entire statement (for assignment)  
 contains at most one critical reference.

**Example:** Fig 0.3

$\ell_2, m_1, m_3$  are LCR-Statements

$\ell_1, m_2$  violate the LCR-requirement

LCR-Program: only LCR-statements

### Interleaved vs. Concurrent Execution

**Claim :** If  $P$  is an LCR program, then the interleaving computations of  $P$  and the concurrent executions of  $P$  give the same results.

Discussion & explanation: *Blue Book*.

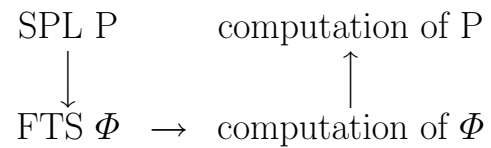
$$P_1 :: \left[ \begin{array}{l} \ell_1: \boxed{b} := \boxed{b} \cdot y_1 \\ \ell_2: \boxed{y_1} := y_1 - 1 \\ \ell_3: \end{array} \right] \quad || \quad P_2 :: \left[ \begin{array}{l} m_1: \mathbf{await} \boxed{y_1} + y_2 \leq n \\ m_2: \boxed{b} := \boxed{b} / y_2 \\ m_3: y_2 := y_2 + 1 \\ m_4: \end{array} \right]$$

Figure 0.3

Critical references

## SPL Semantics

Transition Semantics:



Given an SPL-program  $P$ , we can construct the corresponding FTS  $\Phi = \langle V, \Theta, \mathcal{T}, \mathcal{J}, \mathcal{C} \rangle$ :

- system variables  $V$

$Y = \{y_1, \dots, y_n\}$  – program variables of  $P$   
 domains: as declared in  $P$

$\pi$  – control variable

domain: sets of locations in  $P$

$V = Y \cup \{\pi\}$

## SPL Semantics (Con't)

**Comments:**

- For label  $\ell$ ,  $at\_l: [\ell] \in \pi$   
 $at'_l: [\ell] \in \pi'$

**Note:** When going from an SPL program to an FTS we lose the sequential nature of the program. We need to model control explicitly in the FTS:  $\pi$  can be viewed as a program counter.

## SPL Semantics (Con't)

Example: Fig 0.1

$V = \{\pi, a, b, y_1, y_2, g\}$

$\pi$  - ranges over subsets of

$\{[l_1], [l_2], [l_4], [l_6], [l_7], [l_8]\}$

$a, b, \dots, g$  - range over integers

- Initial Condition  $\Theta$

For  $P :: [\text{dec}; [P_1 :: [l_1: S_1; \hat{l}_1: ] \parallel \dots \parallel P_k :: [l_k: S_k; \hat{l}_k: ]]]$

with data-precondition  $\varphi$ ,

$\Theta: \pi = \{[l_1], \dots, [l_k]\} \wedge \varphi$

Example: Fig 0.1

$\Theta: \pi = \{[l_1]\} \wedge$

$\underbrace{a > 0 \wedge b > 0 \wedge y_1 = a \wedge y_2 = b}_{\text{data-precondition}}$

**in**  $a, b$  : integer where  $a > 0, b > 0$   
**local**  $y_1, y_2$ : integer where  $y_1 = a, y_2 = b$   
**out**  $g$  : integer

$l_1$ : **while**  $y_1 \neq y_2$  **do**  
 $l_2$ :  $\left[ \begin{array}{l} l_2^a: \text{await } y_1 > y_2; l_4: y_1 := y_1 - y_2 \\ \text{or} \\ l_2^b: \text{await } y_2 > y_1; l_6: y_2 := y_2 - y_1 \end{array} \right]$   
 $l_7: g := y_1$   
 $l_8:$

Figure 0.2

A Partially Labeled Program GCD

## SPL Semantics (Con't)

- Transitions  $\mathcal{T}$

$$\mathcal{T} = \{\tau_I\} \cup \left\{ \begin{array}{l} \text{transitions associated with} \\ \text{the statements of } P \end{array} \right\}$$

where  $\tau_I$  is the “idling transition”

$$\rho_I: V' = V$$

abbreviation

$$- \text{pres}(U): \bigwedge_{u \in U} (u' = u) \quad (\text{where } U \subseteq V)$$

the value of  $u \in U$  are preserved

$$- \text{move}(L, \hat{L}): L \subseteq \pi \wedge \pi' = (\pi - L) \cup \hat{L}$$

where  $L, \hat{L}$  are sets of locations

$$- \text{move}(\ell, \hat{\ell}): \text{move}(\{[\ell]\}, \{[\hat{\ell}]\})$$

## SPL Semantics (Con't)

We list the transitions (transition relations) associated with the statements of  $P$

$$\ell : S \qquad \qquad \qquad \rho_\ell$$

**Basic Statements**

$$\ell : \text{skip}; \hat{\ell} : \quad \rightarrow \quad \text{move}(\ell, \hat{\ell}) \wedge \text{pres}(Y)$$

$$\ell : \bar{u} := \bar{e}; \hat{\ell} : \quad \rightarrow \quad \text{move}(\ell, \hat{\ell}) \wedge \bar{u}' = \bar{e} \\ \wedge \text{pres}(Y - \{\bar{u}\})$$

## SPL Semantics (Con't)

### Basic Statements (Con't)

$$\ell: \text{await } c; \widehat{\ell}: \quad \rightarrow \quad \text{move}(\ell, \widehat{\ell}) \wedge c \wedge \text{pres}(Y)$$

$$\begin{aligned} \ell: \text{request } r; \widehat{\ell}: \quad \rightarrow \quad & \text{move}(\ell, \widehat{\ell}) \wedge r > 0 \\ & \wedge r' = r - 1 \\ & \wedge \text{pres}(Y - \{r\}) \end{aligned}$$

$$\begin{aligned} \ell: \text{release } r; \widehat{\ell}: \quad \rightarrow \quad & \text{move}(\ell, \widehat{\ell}) \wedge r' = r + 1 \\ & \wedge \text{pres}(Y - \{r\}) \end{aligned}$$

## SPL Semantics (Con't)

### Basic Statements (Con't)

asynchronous send

$$\begin{aligned} \ell: \alpha \Leftarrow e; \widehat{\ell}: \quad \rightarrow \quad & \text{move}(\ell, \widehat{\ell}) \wedge \alpha' = \alpha \bullet e \\ & \wedge \text{pres}(Y - \{\alpha\}) \end{aligned}$$

asynchronous receive

$$\begin{aligned} \ell: \alpha \Rightarrow u; \widehat{\ell}: \quad \rightarrow \quad & \text{move}(\ell, \widehat{\ell}) \wedge |\alpha| > 0 \\ & \wedge \alpha = u' \bullet \alpha' \\ & \wedge \text{pres}(Y - \{u, \alpha\}) \end{aligned}$$

synchronous send-receive

$$\ell: \alpha \Leftarrow e; \widehat{\ell}: \quad m: \alpha \Rightarrow u; \widehat{m}: \quad$$

$$\text{move}(\{\ell, m\}, \{\widehat{\ell}, \widehat{m}\}) \wedge u' = e \wedge \text{pres}(Y - \{u\})$$

## SPL Semantics (Con't)

### Schematic Statements

$\rho_\ell$

$\ell$ : **noncritical**;  $\widehat{\ell}$ :  $\rightarrow$   $move(\ell, \widehat{\ell}) \wedge pres(Y)$   
 (nontermination modeled by  $\tau_\ell \notin \mathcal{J}$ )

$\ell$ : **critical**;  $\widehat{\ell}$ :  $\rightarrow$   $move(\ell, \widehat{\ell}) \wedge pres(Y)$

## SPL Semantics (Con't)

### Compound Statements

$\ell$ : [**if**  $c$  **then**  $\ell_1: S_1$  **else**  $\ell_2: S_2$ ];  $\widehat{\ell}$ :  $\rightarrow$   
 $\rho_\ell: \rho_\ell^T \vee \rho_\ell^F$  where

$\rho_\ell^T: move(\ell, \ell_1) \wedge c \wedge pres(Y)$

$\rho_\ell^F: move(\ell, \ell_2) \wedge \neg c \wedge pres(Y)$

$\ell$ : [**while**  $c$  **do** [ $\widetilde{\ell}: \widetilde{S}$ ]];  $\widehat{\ell}$ :  $\rightarrow$   
 $\rho_\ell: \rho_\ell^T \vee \rho_\ell^F$  where

$\rho_\ell^T: move(\ell, \widetilde{\ell}) \wedge c \wedge pres(Y)$

$\rho_\ell^F: move(\ell, \widehat{\ell}) \wedge \neg c \wedge pres(Y)$

$\ell$ : [ $[\ell_1: S_1; \widehat{\ell}_1:] \parallel \dots \parallel [\ell_k: S_k; \widehat{\ell}_k:]$ ];  $\widehat{\ell}$ :  $\rightarrow$   
 $\rho_\ell^E: move(\{\ell\}, \{\ell_1, \dots, \ell_k\}) \wedge pres(Y)$  (entry)  
 $\rho_\ell^X: move(\{\widehat{\ell}_1, \dots, \widehat{\ell}_k\}, \{\widehat{\ell}\}) \wedge pres(Y)$  (exit)



## Grouped Statements

$\langle S \rangle$

executed in a single atomic step

Example:

$\langle x := y + 1; z := 2x + 1 \rangle$

$x' = y + 1 \quad \wedge \quad z' = 2y + 3$

the same as  $(x, z) := (y + 1, 2y + 3)$

Example:

$\underbrace{\langle a := 3; a := 5 \rangle}_{a' = 5}$

$a = 3$  is never visible to the outside world, nor to other processes

## SPL Semantics (Con't)

- Justice Set  $\mathcal{J}$

All transitions except

$\tau_I$  and all transitions associated with **noncritical** statements

- Compassion Set  $\mathcal{C}$

All transitions associated with

send, receive, request statements

## Computations of Programs

$$P_1 :: \left[ \begin{array}{l} \text{local } x: \text{ integer where } x = 1 \\ \ell_0: \left[ \begin{array}{l} \ell_0^a: \text{ await } x = 1 \\ \text{ or} \\ \ell_0^b: \text{ skip} \end{array} \right] \\ \ell_1: \end{array} \right] \parallel P_2 :: \left[ \begin{array}{l} m_0: \text{ while } \top \text{ do} \\ [m_1: x := -x] \end{array} \right]$$

Fig 0.4 Process  $P_1$  terminates in all computations.

$$\begin{aligned} \sigma: \langle \pi: \{\ell_0, m_0\}, x: 1 \rangle &\xrightarrow{m_0} \langle \pi: \{\ell_0, m_1\}, x: 1 \rangle \xrightarrow{m_1} \\ \langle \pi: \{\ell_0, m_0\}, x: -1 \rangle &\xrightarrow{m_0} \langle \pi: \{\ell_0, m_1\}, x: -1 \rangle \xrightarrow{m_1} \\ \langle \pi: \{\ell_0, m_0\}, x: 1 \rangle &\xrightarrow{m_0} \dots \end{aligned}$$

$\sigma$  is not a computation. Unjust towards  $\ell_0^b$   
(enabled on all states but never taken)

## Computations of Programs (Con't)

$$P_1 :: \left[ \begin{array}{l} \text{local } x: \text{ integer where } x = 1 \\ \ell_0: \left[ \begin{array}{l} \ell_0^a: \text{ await } x = 1 \\ \text{ or} \\ \ell_0^b: \text{ await } x \neq 1 \end{array} \right] \\ \ell_1: \end{array} \right] \parallel P_2 :: \left[ \begin{array}{l} m_0: \text{ while } \top \text{ do} \\ [m_1: x := -x] \end{array} \right]$$

Fig 0.5 **skip**  $\rightarrow$  **await**  $x \neq 1$

$$\begin{aligned} \sigma: \langle \pi: \{\ell_0, m_0\}, x: 1 \rangle &\xrightarrow{m_0} \langle \pi: \{\ell_0, m_1\}, x: 1 \rangle \xrightarrow{m_1} \\ \langle \pi: \{\ell_0, m_0\}, x: -1 \rangle &\xrightarrow{m_0} \langle \pi: \{\ell_0, m_1\}, x: -1 \rangle \xrightarrow{m_1} \\ \langle \pi: \{\ell_0, m_0\}, x: 1 \rangle &\xrightarrow{m_0} \dots \end{aligned}$$

$\sigma$  is a computation –  
since none of the just transitions are  
continually enabled.

## Computations of Programs (Con't)

**local  $x$ : integer where  $x = 1$**

$$P_1 :: \left[ \begin{array}{l} \ell_0: \text{if } x = 1 \text{ then} \\ \quad \ell_1: \text{skip} \\ \text{else} \\ \quad \ell_2: \text{skip} \\ \ell_3: \end{array} \right] \parallel P_2 :: \left[ \begin{array}{l} m_0: \text{while } \top \text{ do} \\ \quad [m_1: x := -x] \end{array} \right]$$

Fig 0.6 Process  $P_1$  terminates in all computations.

$$\begin{aligned} \sigma: \langle \pi: \{\ell_0, m_0\}, x: 1 \rangle &\xrightarrow{m_0} \langle \pi: \{\ell_0, m_1\}, x: 1 \rangle \xrightarrow{m_1} \\ \langle \pi: \{\ell_0, m_0\}, x: -1 \rangle &\xrightarrow{m_0} \langle \pi: \{\ell_0, m_1\}, x: -1 \rangle \xrightarrow{m_1} \\ \langle \pi: \{\ell_0, m_0\}, x: 1 \rangle &\xrightarrow{m_0} \dots \end{aligned}$$

$\sigma$  is not a computation –  
 since  $\ell_0$  is continually enabled,  
 but not taken.

## Control Configurations

$L = \{[\ell_1], \dots, [\ell_k]\}$  of  $P$  is called conflict-free  
 if no  $[\ell_i]$  conflicts with  $[\ell_j]$ , for  $i \neq j$ .

$L$  is called a (control) configuration of  $P$   
 if it is a maximal conflict-free set.

**Example:**

**local  $x$ : integer where  $x = 0$**

$$P_1 :: \left[ \begin{array}{l} \ell_0: x := 1 \\ \ell_1: \end{array} \right] \parallel P_2 :: \left[ \begin{array}{l} m_0: \text{await } x = 1 \\ m_1: \end{array} \right]$$

Configurations

$$\begin{aligned} &\{[\ell_0], [m_0]\}, \{[\ell_0], [m_1]\}, \\ &\{[\ell_1], [m_0]\}, \{[\ell_1], [m_1]\} \end{aligned}$$

## SPL Semantics (Con't)

accessible configuration –  
appears as value of  $\pi$  in some accessible state

**Example:**

$\{[l_0], [m_1]\}$  does not appear in any accessible state

Is a given configuration accessible?

Undecidable

## The Mutual-Exclusion Problem

loop forever do      loop forever do

noncritical		noncritical
.....		.....
critical		critical
.....		.....

Requirements:

- Exclusion

While one of the processes is in its critical section, the other is not

- Accessibility

Whenever a process is at the noncritical section exit, it must eventually reach its critical section

**Example:** mutual exclusion by semaphores

Fig. 0.7

## Message-Passing Programs

**Example:** Producer-Consumer

Fig. 0.9

assumption:

channel send  $\leq N$  values

local  $y$ : integer where  $y = 1$

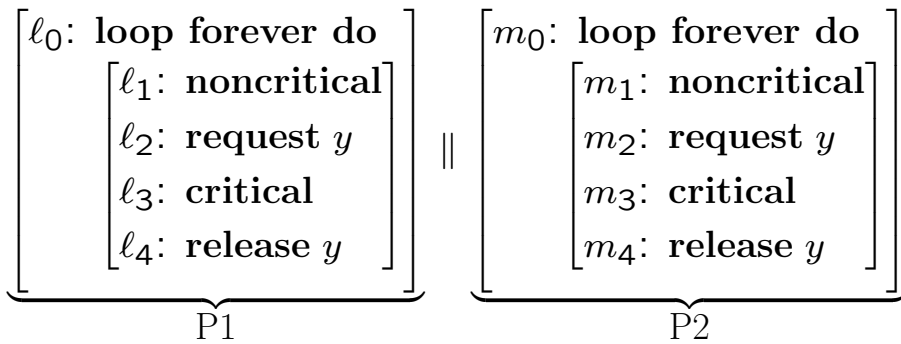


Fig. 0.7 Program MUX-SEM

local  $send, ack$ : channel [1..] of integer

where  $send = \Lambda$ ,  $ack = \underbrace{[1, \dots, 1]}_N$

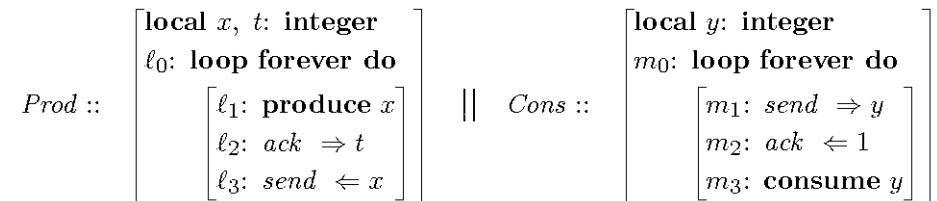


Fig. 0.9 Program PROD-CONS