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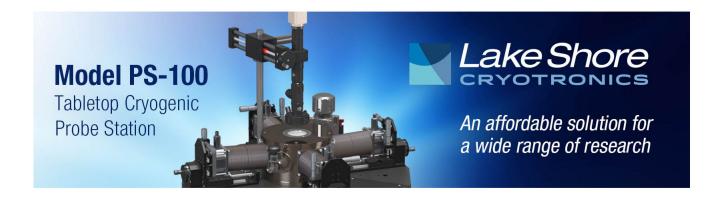
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## A 2-terminal perovskite/silicon multijunction solar cell enabled by a silicon tunnel junction

Jonathan P. Mailoa, <sup>1,a),b)</sup> Colin D. Bailie, <sup>2,a)</sup> Eric C. Johlin, <sup>1</sup> Eric T. Hoke, <sup>2</sup> Austin J. Akey, <sup>1</sup> William H. Nguyen, <sup>2</sup> Michael D. McGehee, <sup>2,b)</sup> and Tonio Buonassisi <sup>1,b)</sup> <sup>1</sup> Massachusetts Institute of Technology, Cambridge, Massachusetts 02139, USA <sup>2</sup> Stanford University, Stanford, California 94305, USA

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With the advent of efficient high-bandgap metal-halide perovskite photovoltaics, an opportunity exists to make perovskite/silicon tandem solar cells. We fabricate a monolithic tandem by developing a silicon-based interband tunnel junction that facilitates majority-carrier charge recombination between the perovskite and silicon sub-cells. We demonstrate a  $1\,\mathrm{cm}^2$  2-terminal monolithic perovskite/silicon multijunction solar cell with a  $V_{\mathrm{OC}}$  as high as 1.65 V. We achieve a stable 13.7% power conversion efficiency with the perovskite as the current-limiting sub-cell, and identify key challenges for this device architecture to reach efficiencies over 25%. © 2015 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4914179]

Solar-to-electricity conversion efficiency is the technical variable that most strongly influences silicon (Si) photovoltaic (PV) module costs. 1,2 The record efficiency of crystalline silicon (c-Si) single-junction PV devices has increased from 25% to 25.6% during the last fifteen years, 3,4 asymptotically approaching the 29.4% Auger-recombination-constrained Shockley-Queisser limit.<sup>5</sup> To make PV modules with higher efficiency than market-leading c-Si while leveraging existing c-Si manufacturing capacity, Si-based tandem approaches have been proposed. 6-10 The top sub-cell in a silicon-based tandem should have a band gap between 1.6 and 1.9 eV. 11 However, very few materials exhibit high open-circuit voltages  $(V_{OC})$  within this band gap range. Recently, the methylammonium-lead-halide perovskite has demonstrated a rapid efficiency increase  $^{12-16}$  with a  $V_{\rm OC}$  of 1.15 V.  $^{17}$  The methylammonium-lead-halide perovskite has a tunable band gap, ranging from 1.6 to 2.3 eV depending on halide composition, 18 though not all compositions are currently stable under illumination.<sup>19</sup> Methods to optically transmit longer-wavelength light through a top perovskite sub-cell in a mechanically stacked tandem configuration have been developed recently.9,20

A perovskite/Si multijunction solar cell may also be constructed via monolithic integration where a thin film perovskite sub-cell is deposited directly onto the c-Si sub-cell. Monolithic integration requires electrical coupling between sub-cells and transmission of infrared light to the bottom sub-cell. We use an interband tunnel junction<sup>21</sup> to facilitate electron tunneling from the electron-selective contact of the perovskite sub-cell into the p-type emitter of the Si sub-cell. This approach stands in contrast to the recombination layer used in other perovskite tandem systems<sup>22</sup> and is the one widely used in III-V<sup>23</sup> and micromorph (a-Si/ $\mu$ c-Si)<sup>24</sup> tandem solar cells. Unlike the tunnel junction in III-V multijunction solar cells, our tunnel junction is made of silicon with an indirect band gap, enabling electrical coupling

with minimal parasitic absorption. The conduction-band alignment between Si and the perovskite sub-cell's electron-selective contact (TiO<sub>2</sub>) enables bypassing the usage of a transparent conducting oxide (TCO) recombination layer, an alternative option with greater parasitic absorption. In this work, we use the methylammonium-lead(II)-iodide perovskite (CH<sub>3</sub>NH<sub>3</sub>PbI<sub>3</sub>), which has a 1.61 eV band gap.<sup>25</sup> Herein, we present the device design, fabrication, characterization, and loss analysis of monolithic perovskite/Si multijunction solar cells.

We develop a 2-terminal perovskite/Si multijunction architecture on an n-type Si solar cell (Figure 1(a)) with 1 cm<sup>2</sup>

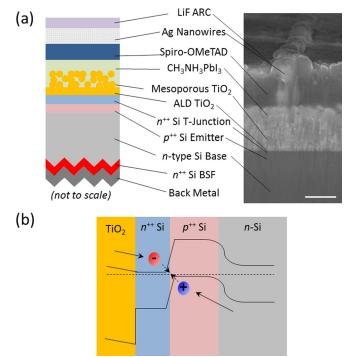


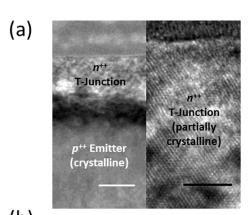
FIG. 1. (a) The device structure of a 2-terminal monolithically grown perovskite/Si multijunction solar cell with an *n*-type Si base. The polished SEM image is taken at 45° tilt to show the Ag nanowire mesh (500 nm scale bar). (b) Band diagram of the perovskite/silicon cell interface showing the charge-transport mechanism around the Si tunnel junction.

<sup>&</sup>lt;sup>a)</sup>J. P. Mailoa and C. D. Bailie contributed equally to this work

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area. We process an *n*-type Si sub-cell with planar top surface and full-area p-type emitter and n-type back surface field (BSF) using standard Si processing techniques.<sup>26</sup> A tunnel junction facilitates carrier recombination (holes from the n-type Si base passing through the p-type emitter and electrons from the perovskite layer passing through its TiO2 electron transport layer, respectively, as shown in Figure 1(b)). We form an  $n^{++}/p^{++}$  tunnel junction by depositing heavily doped  $n^{++}$  hydrogenated amorphous silicon (a-Si:H) using plasma-enhanced chemical vapor deposition (PECVD).<sup>26</sup> The 30 nm-thick a-Si:H layer is subsequently annealed in N2 ambient at 680 °C for 15 min to activate the dopants.<sup>27</sup> It is known that interdiffusion of dopant species during the device fabrication process (such as the dopant activation anneal) may degrade the tunnel junction conductivity. Accordingly, a 2–3 nm-thick intrinsic a-Si layer is inserted between the  $p^{++}$ emitter and the  $n^{++}$  amorphous Si layer during the PECVD process<sup>26</sup> to mitigate possible dopant interdiffusion.<sup>28</sup> After the dopant-activation anneal, the amorphous layers are partially crystallized as shown by transmission electron microscopy (TEM, Figure 2(a)). Using secondary ion mass spectrometry (SIMS), we show the dopant concentration on the  $n^{++}/p^{++}$  Si interface after the dopant activation anneal is  $10^{19}$ – $10^{20}$  cm<sup>-3</sup>, which is suitable to form a high-quality interband tunnel junction (Figure 2(b)).<sup>28</sup>

The addition of the tunnel junction on top of the single-junction n-type solar cell slightly reduces the short-circuit current  $(J_{SC})$ , but the presence of the tunnel junction has a



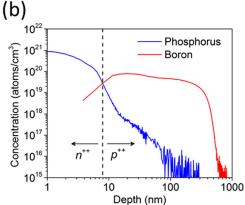


FIG. 2. (a) TEM image of the  $n^{++}/p^{++}$  silicon tunnel junction interface after the dopant activation annealing (left: 30 nm scale bar) and high-resolution TEM image of the  $n^{++}$  layer, showing the partially crystalline nature of this layer (right: 5 nm scale bar). (b) SIMS profile of the Si emitter and tunnel junction layer showing the sharp doping profile at the tunnel junction interface.

negligible effect on the series resistance (Figure S1) indicating proper operation of the tunnel juntion. The slight reduction in  $J_{\rm SC}$  is due to parasitic absorption at  $\lambda < 500\,\mathrm{nm}$  (Figure S2), this portion of the spectrum is absorbed in the perovskite cell before reaching the tunnel junction.

The efficiency of the planar single-junction Si cells is 13.8%. This efficiency is lower than commercial averages in part due to tandem design considerations. These intentional design considerations are: (1) No surface texturing for light trapping is applied because a planar front surface simplifies deposition of the perovskite; (2) No *p*-type front surface passivation scheme is applied on the emitter because the same technique cannot be implemented on the *n*-type portion of the tunnel junction. Other causes of lower efficiency are: (1) full-area BSF passivation only provides moderate passivation; (2) a front surface passivation scheme that can be decoupled from the tunnel junction formation needs to be developed; (3) dedicated clean furnaces for emitter formation and back surface passivation are necessary to make more efficient Si sub-cells.

We fabricate the monolithic perovskite/Si multijunction solar cell by depositing a perovskite sub-cell on top of the tunnel junction. We first metallize the back of our Si subcell, then deposit a 30 nm-thick TiO2 layer on the planar  $n^{++}$  c-Si front surface using atomic layer deposition (ALD). <sup>26</sup> This  $TiO_2$  layer is the *n*-type heterojunction for the perovskite sub-cell. It is known that TiO2 is a good electronselective contact for c-Si because of its conduction-band alignment, 29 which also eases electron transport from the  $TiO_2$  layer into the  $n^{++}$  Si tunnel junction layer. <sup>30,31</sup> The thickness of this ALD TiO2 layer can impact the optical transmission into the Si sub-cell (Figure S3),26 and hence further thickness optimization is needed. The perovskite is deposited onto a mesoporous TiO2 layer in a two-step conversion method. 15,26 An organic p-type heterojunction contact, spiro-OMeTAD, is deposited by spin-coating on top of the perovskite and is chemically doped with spiro-(TSFI)<sub>2</sub>.<sup>32</sup> The top silver nanowire (AgNW) electrode is deposited using the procedure developed by Bailie et al. AgNWs are sprayed from solution onto a plastic (PET) film, and then transferred from the PET to the spiro-OMeTAD layer via mechanical transfer through the application of pressure via a ball bearing. The silver nanowire electrode before transfer has a sheet resistance of 9  $\Omega/\Box$  with a peak transmission of 89.5%. A 111-nm-thick lithium fluoride (LiF) anti-reflection layer is then deposited by thermal evaporation.

The J-V curve of our 2-terminal perovskite/Si multijunction solar cell under simulated AM1.5G illumination is shown in Figure 3(a). Due to the hysteresis often observed in metal-halide perovskite solar cells, it is important to be rigorous with J-V characterization. We use a 5 s delay after each 100 mV voltage step before measuring the current in both scan directions. However, we still find hysteresis at this scan rate and that up to 30 s is required to reach a steady-state. This yields an overestimation of the efficiency when scanning from forward to reverse bias and an underestimation when scanning from reverse to forward bias. We measure the steady-state values of the three critical points on the J-V curve: open circuit (V<sub>OC</sub>), short circuit (J<sub>SC</sub>), and the

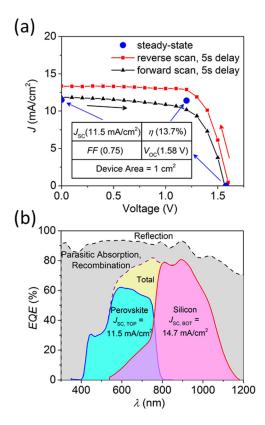


FIG. 3. (a) J-V curve of the 2-terminal perovskite/silicon multijunction solar cell under AM1.5G illumination. Forward and reverse-bias scan directions are shown with 5 s measurement delay per data point. Steady-state values for  $J_{SC}$ ,  $V_{OC}$ , and MPP are measured by averaging over 30 s after reaching steady state. (b) Total device reflection and EQE of the perovskite and Si sub-cells of a typical perovskite/Si multijunction cell. The perovskite sub-cell EQE is corrected to match the measured  $J_{SC}$  and the silicon sub-cell EQE is reported as measured. <sup>26</sup>

maximum power point (MPP), depicted as blue circles (color online) in Figure 3(a) to negate hysteretic effects. The steady-state  $J_{\rm SC}$  is 11.5 mA/cm², the steady-state  $V_{\rm OC}$  is 1.58 V, and the steady-state MPP is 13.7% at 1.20 V bias (Figure S4).<sup>26</sup> These resultant fill factor (*FF*) is 0.75. The 1 cm² cell was aperture-masked to ensure a correct illumination area. The  $V_{\rm OC}$  has been measured as high as 1.65 V in some devices. This result is encouraging, as the  $V_{\rm OC}$  is approximately the sum of the  $V_{\rm OC}$  for the perovskite top sub-cell and the bottom Si sub-cell illuminated through a separate semi-transparent perovskite device on FTO9 (approximately 1.05 V and 0.55 V, respectively), further indicating proper operation of the tunnel junction.

The slow current-dynamics and corresponding hysteresis observed in the tandem resemble the sluggish dynamics of our perovskite solar cells and suggest that the perovskite sub-cell limits the current of the tandem. This finding is substantiated by external quantum efficiency (EQE) measurements (Figure 3(b)) of the individual sub-cells. Our tandem  $J_{SC}$  of  $11.5\,\mathrm{mA/cm^2}$  is low because the perovskite is illuminated through the p-type heterojunction, opposite from conventional perovskite devices. To understand the directional dependence of illumination on the perovskite sub-cell, we illuminate a semi-transparent single-junction perovskite solar cell through the TiO<sub>2</sub> heterojunction and through the spiro-OMeTAD heterojunction. When illuminated through

the TiO<sub>2</sub> heterojunction, the EQE of the semi-transparent cell integrates to 17.3 mA/cm<sup>2</sup>, whereas when illuminated through the spiro-OMeTAD heterojunction the EQE integrates to 11.4 mA/cm<sup>2</sup> (Figure S5) due to low EQE for  $\lambda < 550$  nm.<sup>26</sup> We attribute the lower photocurrent from the spiro-OMeTAD-side to parasitic absorption by the doped spiro-OMeTAD layer. From absorption measurements of doped spiro-OMeTAD on glass (Figure S6),<sup>26</sup> we estimate that the absorbed flux of the AM1.5G spectrum in this layer is 6.4 mA/cm<sup>2</sup> from 300–750 nm. We find that parasitic absorption by spiro-OMeTAD also reduces the photocurrent available to the bottom Si sub-cell, absorbing the equivalent of 2.0 mA/cm<sup>2</sup> of infrared photons from 750–1200 nm. The parasitic absorption can be reduced with a thinner spiro-OMeTAD layer, which is currently optimized to planarize a rough perovskite top surface to enable deposition of the silver nanowire electrode. With smoother perovskite films, the spiro-OMeTAD layer can achieve planarization with a thinner layer. The parasitic absorption may be completely removed by replacement of spiro-OMeTAD with an alternate p-type heterojunction contact that simultaneously exhibits both good conductivity and low parasitic absorption.

The perovskite sub-cell is also limited by quality of the perovskite absorber. Our single-junction perovskite cells with gold back contacts fabricated as control devices achieve at best 13.5% efficiency. Optimization of deposition conditions, precursor materials, and annealing protocols along with replacement or reduction of the spiro-OMeTAD layer is expected to yield a perovskite top sub-cell equivalent to the record single-junction perovskite cell, which currently stands at 20.1%.3 For the Si sub-cell, applying a back-surface field and excellent surface passivation to the back of the bottom Si sub-cell, using dedicated furnaces, and decoupling the front surface passivation scheme from the tunnel junction formation are expected to yield improvements. We predict a Si sub-cell with a  $V_{\rm OC}$  of 660–720 mV and a matched tandem  $J_{\rm SC}$  of 18–19 mA/cm<sup>2</sup> when illuminated through the perovskite sub-cell. We expect, with these changes designed to improve the sub-cells to the match the highest quality devices available today, the monolithic tandem would have a  $V_{\rm OC}$  of 1.84 V, a  $J_{\rm SC}$  of 19 mA/cm<sup>2</sup>, a FF of 0.83, and a corresponding efficiency of 29.0%. Ultimately, it has been suggested that perovskite/Si monolithic tandems can surpass 35% efficiency through careful photon management. 10

In summary, we have demonstrated a 1 cm<sup>2</sup>, 2-terminal monolithically-integrated metal-halide perovskite/Si multijunction solar cell. The monolithic integration is enabled by a Si-based tunnel junction fabricated directly on top of the bottom Si sub-cell emitter and by incorporating a semitransparent silver nanowire-based top electrode. We obtained a multijunction device  $V_{\rm OC}$  as high as 1.65 V, which is the expected sum of the perovskite and filtered Si  $V_{\rm OC}$ 's, demonstrating the potential of this approach. The best 2-terminal multijunction prototype device efficiency is 13.7% and is current-limited by the perovskite sub-cell. This value is low compared to the record efficiency for perovskite or Si cells, in part because this tandem prototype does not yet have bestin-class perovskite and Si layers. Further improvements can be achieved by replacing the spiro-OMeTAD layer with wider band gap hole transport material, improving the quality of the perovskite absorber, use of dedicated furnaces for the Si sub-cell fabrication, and by implementing better surface passivation schemes on the front and back side of the Si sub-cell. These improvements can yield a 29.0% efficient tandem, with the ultimate efficiency potential of these monolithic tandems surpassing 35%.

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