CS 106B

Lecture 27: A* Heuristics and Minimum Spanning Trees

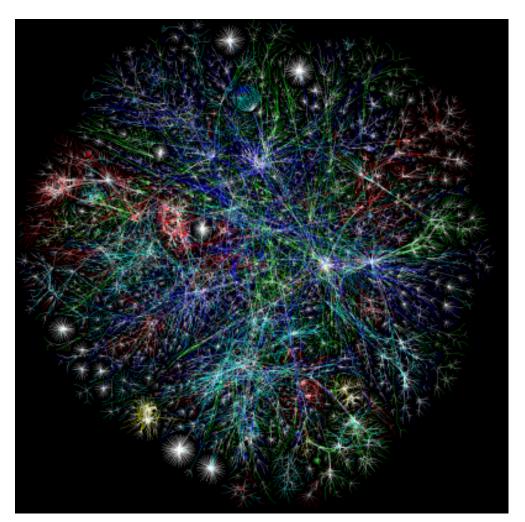
Friday, December 2, 2016

Programming Abstractions
Fall 2016
Stanford University
Computer Science Department

Lecturer: Chris Gregg

reading:

Programming Abstractions in C++, pp. 820-821





Today's Topics

- Logistics
- •We updated the Trailblazer alternate route description on the handout.
- Typedef definition
- •Real Graph: Internet routers and traceroute
- More on Trailblazer
- A* Heuristics
- •heuristic bounds, and why we want to underestimate
- Minimum Spanning Trees
- Kruskal's algorithm
- Dijkstra and negative weights (extra slides)



C++: typedef

In Assignment 7, you will use a **Vector<Vertex** *> type for all your paths. Because paths are used so often in the assignment, we have defined a type called **Path**, which is simply a **Vector<Vertex** *>. To define a new type in C++, you can use the **typedef** keyword, as follows:

```
typedef Vector<Vertex *> Path;
```

Now, you can use Path as any other variable, and it is just a **vector<vertex *>**:

```
Path p;
if (p.size() > 0) {
    Vertex *v = p[0];
}
for (Vertex *v : p) {
    cout << "Next vertex name: " << v->name << endl;
}</pre>
```



Real Graphs!

There was a Tiny Feedback from the last lecture that said,

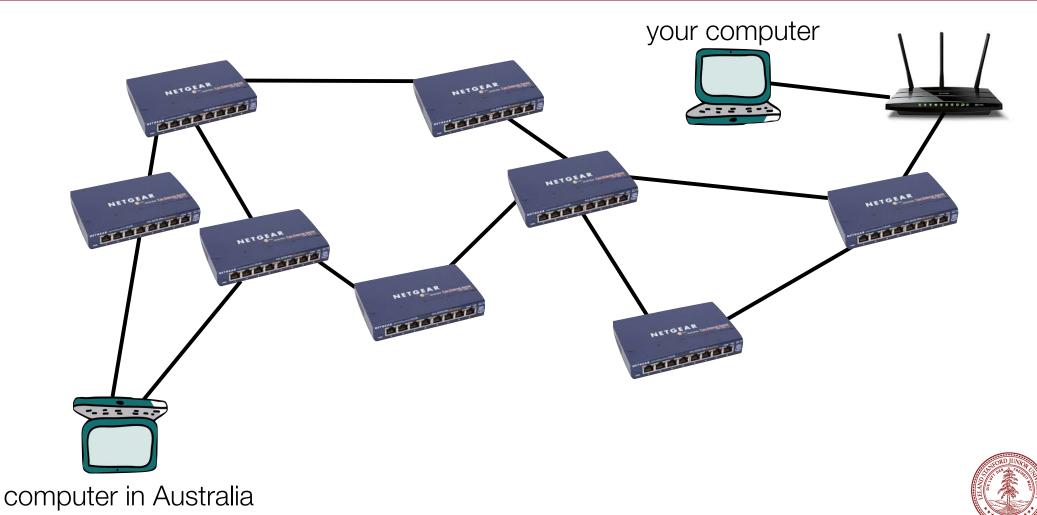
"Would love more stories about when you two use these different search algorithms more in real life."

On Monday we played the "Wikipedia Get to Philosophy" game with the Internet, which have web pages with links that form a graph. Let's see another example of how the Internet is a real graph in a completely different way: Routers

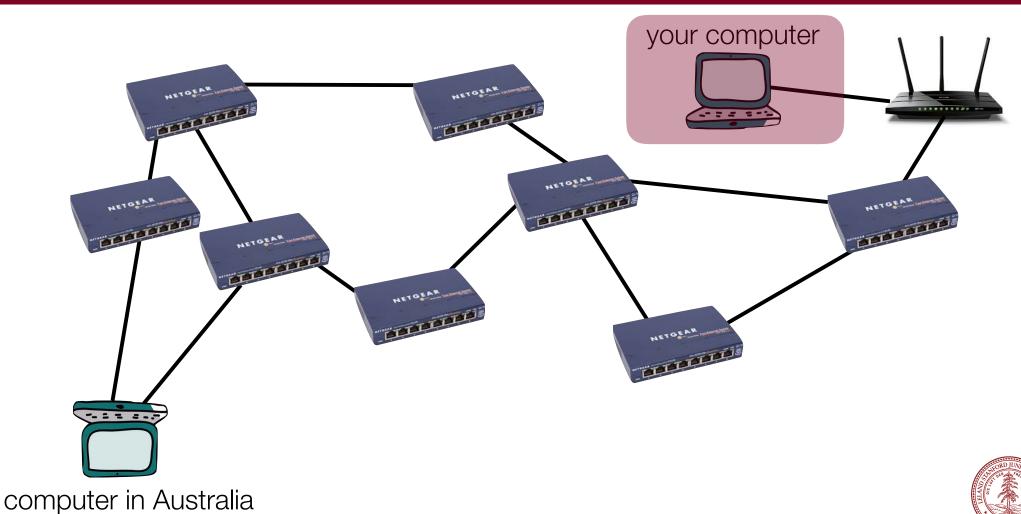
How does a message get sent from your computer to another computer on the Internet, say in Australia?



The Internet: Computers connected through routers



The Internet: Computers connected through routers



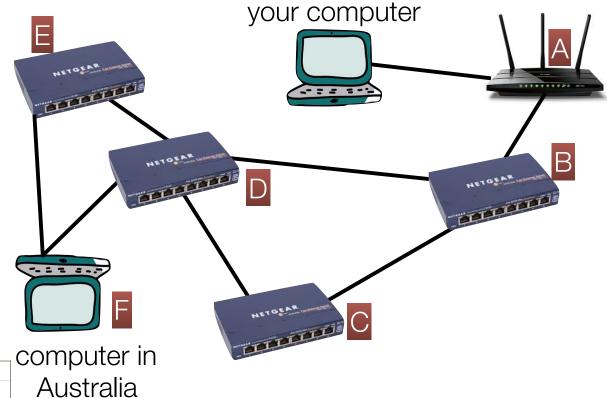
The destination computer has a name and an IP address, like this:

www.engineering.unsw.edu.au IP address: 149.171.158.109

The first number denotes the "network address" and routers continually pass around information about how many "hops" they think it will take for them to get to all the networks.

E.g., for router **C**:

router	hops
A	2
В	1
С	_
D	1
E	2
F	2





Each router knows its neighbors, and it has a copy of its neighbors' tables. So, **B** would have the following tables:

A

router	hops
A	_
В	1
С	3
D	2
E	3
F	3

hops

NETOEAR	your computer	A
NETOEAR	D	METOEAR B
F	NETOEAR C	

	A	2
1	В	1
•	С	_
	D	1
	E	2
	F	2

router

D A 2
B 1
C 1
D E 1

hops

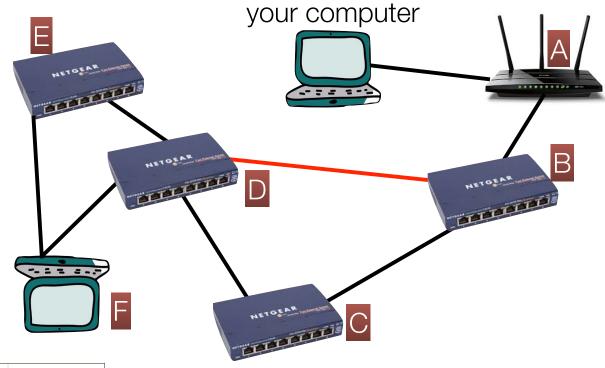
router



If B wants to connect to F, it connects through its neighbor that reports the shortest path to F. Which router would it choose?

A

router	hops
A	_
В	1
С	3
D	2
E	3
F	3



router	hops
A	2
В	1
С	_
D	1
E	2
F	2

router hops
A 2
B 1
C 1
D E 1
F 1

D



If B wants to connect to F, it connects through its neighbor that reports the shortest path to F. Which router would it choose? D.

A

router	hops
A	_
В	1
С	3
D	2
E	3
F	3

METOEAR	your computer	A
NETOFAR	D	NETOEAR BEREIE
	NETOEAR C	

Volum compliter

hops
2
1
_
1
2
2

	router	hops
	A	2
	В	1
D	С	1
	D	_
	E	1
	F	1



Traceroute

We can use a program called "traceroute" to tell us the path between our computer and a different computer:

traceroute -I -e www.engineering.unsw.edu.au





Traceroute: Stanford Hops

```
traceroute -I -e www.engineering.unsw.edu.au
traceroute to www.engineering.unsw.edu.au (149.171.158.109), 64 hops max, 72 byte packets
   csmx-west-rtr.sunet (171.67.64.2) 7.414 ms 9.155 ms 8.288 ms
   gnat-2.sunet (172.24.70.12) 0.339 ms 1.532 ms 0.423 ms
   csmx-west-rtr-v13866.sunet (171.64.66.2) 38.916 ms 10.506 ms 8.402 ms
   dca-rtr-vlan8.sunet (171.64.255.204) 0.530 ms 0.521 ms 0.713 ms
   dc-svl-agg4--stanford-10ge.cenic.net (137.164.50.157) 1.554 ms 1.653 ms 2.828 ms
 5
   hpr-svl-hpr2--svl-agg4-10ge.cenic.net (137.164.26.249) 1.212 ms 1.161 ms 1.204 ms
   aarnet-2-is-jmb-778.sttlwa.pacificwave.net (207.231.245.4) 17.994 ms 17.998 ms
 7
   et-2-0-0.pe2.brwy.nsw.aarnet.net.au (113.197.15.98) 160.020 ms 160.234 ms 159.922 ms
   et-3-3-0.pel.brwy.nsw.aarnet.net.au (113.197.15.148) 160.285 ms 160.076 ms 160.118 ms
   138.44.5.1 (138.44.5.1) 160.124 ms
10
                                      160.138 ms
                                                   160.068 ms
11
   ombcr1-te-1-5.gw.unsw.edu.au (149.171.255.106)
                                                  160.090 ms 160.381 ms 160.185 ms
   rldcdnex1-po-2.gw.unsw.edu.au (149.171.255.178) 160.909 ms 160.847 ms 160.921 ms
12
13
   dcfw1-ae-1-3049.gw.unsw.edu.au (129.94.254.60) 160.592 ms 160.558 ms 160.949 ms
   www.engineering.unsw.edu.au (149.171.158.109)
                                                 160.978 ms 161.184 ms
14
                                                                         160.987 ms
```



Traceroute: CENIC

```
traceroute -I -e www.engineering.unsw.edu.au
traceroute to www.engineering.unsw.edu.au (149.171.158.109), 64 hops max, 72 byte packets
   csmx-west-rtr.sunet (171.67.64.2) 7.414 ms 9.155 ms 8.288 ms
   gnat-2.sunet (172.24.70.12) 0.339 ms 1.532 ms 0.423 ms
 2
   csmx-west-rtr-v13866.sunet (171.64.66.2) 38.916 ms 10.506 ms 8.402 ms
   dca-rtr-vlan8.sunet (171.64.255.204) 0.530 ms 0.521 ms 0.713 ms
  dc-svl-agg4--stanford-10ge.cenic.net (137.164.50.157) 1.554 ms 1.653 ms
   hpr-svl-hpr2--svl-agg4-10ge.cenic.net (137.164.26.249) 1.212 ms 1.161 ms 1.204 ms
   aarnet-2-is-jmb-778.sttlwa.pacificwave.net (207.231.245.4)
                                                              17.994 ms
 7
                                                                         17.998 ms
   et-2-0-0.pe2.brwy.nsw.aarnet.net.au (113.197.15.98) 160.020 ms 160.234 ms
                                                                               159.922 ms
   et-3-3-0.pe1.brwy.nsw.aarnet.net.au (113.197.15.148) 160.285 ms 160.076 ms 160.118 ms
   138.44.5.1 (138.44.5.1) 160.124 ms
10
                                       160.138 \, \mathrm{ms}
                                                   160.068 ms
11
   ombcr1-te-1-5.gw.unsw.edu.au (149.171.255.106)
                                                   160.090 ms 160.381 ms 160.185 ms
   rldcdnex1-po-2.gw.unsw.edu.au (149.171.255.178) 160.909 ms 160.847 ms 160.921 ms
12
13
   dcfw1-ae-1-3049.gw.unsw.edu.au (129.94.254.60) 160.592 ms 160.558 ms 160.949 ms
   www.engineering.unsw.edu.au (149.171.158.109)
                                                  160.978 ms 161.184 ms 160.987 ms
14
```

The Corporation for Education Network Initiatives in California (CENIC) is a nonprofit corporation formed in 1996 to provide high-performance, high-bandwidth networking services to California universities and research institutions (source: Wikipedia)

Traceroute: Pacificwave (Seattle)

```
traceroute -I -e www.engineering.unsw.edu.au
traceroute to www.engineering.unsw.edu.au (149.171.158.109), 64 hops max, 72 byte packets
   csmx-west-rtr.sunet (171.67.64.2) 7.414 ms 9.155 ms 8.288 ms
   gnat-2.sunet (172.24.70.12) 0.339 ms 1.532 ms 0.423 ms
2
   csmx-west-rtr-v13866.sunet (171.64.66.2) 38.916 ms 10.506 ms 8.402 ms
  dca-rtr-vlan8.sunet (171.64.255.204) 0.530 ms 0.521 ms 0.713 ms
   dc-svl-agg4--stanford-10ge.cenic.net (137.164.50.157) 1.554 ms 1.653 ms 2.828 ms
6 hpr-svl-hpr2--svl-agg4-10ge.cenic.net (137.164.26.249) 1.212 ms 1.161 ms 1.204 ms
   aarnet-2-is-jmb-778.sttlwa.pacificwave.net (207.231.245.4) 17.994 ms 17.998 ms
   et-2-0-0.pe2.brwy.nsw.aarnet.net.au (113.197.15.98) 160.020 ms 160.234 ms
   et-3-3-0.pel.brwy.nsw.aarnet.net.au (113.197.15.148) 160.285 ms 160.076 ms 160.118 ms
   138 44 5 1 (138.44.5.1) 160.124 ms
                                    160.138 ms
                                               160.068 ms
                gw.unsw.edu.au (149.171.255.106) 160.090 ms 160.381 ms
                                                                      160.185 ms
                .gw.unsw.edu.au (149.171.255.178) 160.909 ms 160.847 ms 160.921 ms
    NORTHWEST
                g.unsw.edu.au (149.171.158.109)
                                               160.978 ms 161.184 ms
                                                                     160.987 ms
                  Pass Internet traffic directly with other major national and international
```

GIGAPOP

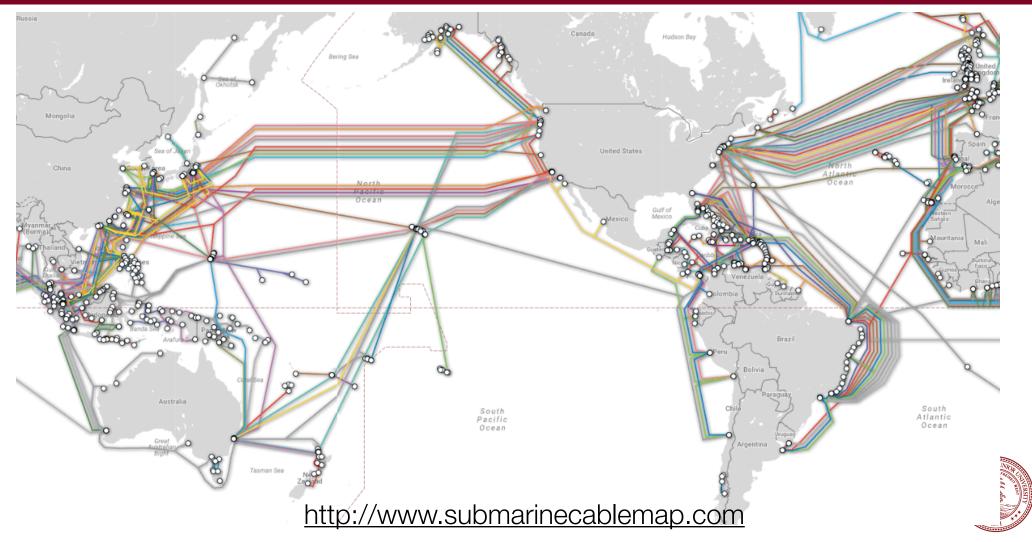
exchange/)

networks, including U.S. federal agencies and many Pacific Rim R&E

networks (source: http://www.pnwqp.net/services/pacific-wave-peering-



Traceroute: Oregon to Australia - underwater!



Traceroute: Australia

```
traceroute -I -e www.engineering.unsw.edu.au
traceroute to www.engineering.unsw.edu.au (149.171.158.109), 64 hops max, 72 byte packets
   csmx-west-rtr.sunet (171.67.64.2) 7.414 ms 9.155 ms 8.288 ms
   gnat-2.sunet (172.24.70.12) 0.339 ms 1.532 ms 0.423 ms
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3
   csmx-west-rtr-v13866.sunet (171.64.66.2) 38.916 ms 10.506 ms 8.402 ms
   dca-rtr-vlan8.sunet (171.64.255.204) 0.530 ms 0.521 ms 0.713 ms
   dc-svl-agg4--stanford-10ge.cenic.net (137.164.50.157) 1.554 ms 1.653 ms 2.828 ms
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   aarnet-2-is-jmb-778.sttlwa.pacificwave.net (207.231.245.4) 17.994 ms 17.998 ms 18.319 ms
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   et-3-3-0.pel.brwy.nsw.aarnet.net.au (113.197.15.148) 160.285 ms 160.076 ms 160.118 ms
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   138.44.5.1 (138.44.5.1) 160.124 ms 160.138 ms 160.068 ms
                                                  160.090 ms 160.381 ms 160.185 ms
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   rldcdnex1-po-2.gw.unsw.edu.au (149.171.255.178) 160.909 ms 160.847 ms 160.921 ms
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   www.engineering.unsw.edu.au (149.171.158.109) 160.978 ms 161.184 ms 160.987 ms
14
```



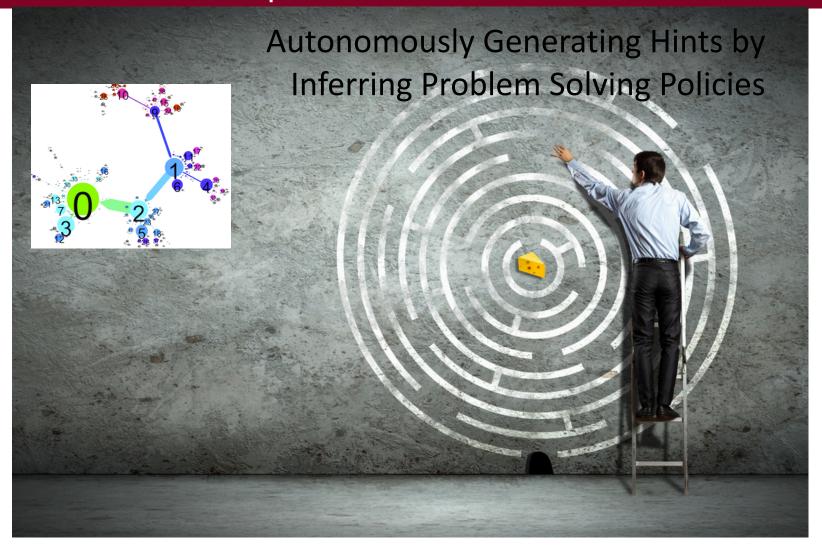
Traceroute: University of New South Wales

```
traceroute -I -e www.engineering.unsw.edu.au
traceroute to www.engineering.unsw.edu.au (149.171.158.109), 64 hops max, 72 byte packets
   csmx-west-rtr.sunet (171.67.64.2) 7.414 ms 9.155 ms 8.288 ms
   gnat-2.sunet (172.24.70.12) 0.339 ms 1.532 ms 0.423 ms
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   csmx-west-rtr-v13866.sunet (171.64.66.2) 38.916 ms 10.506 ms 8.402 ms
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   et-2-0-0.pe2.brwy.nsw.aarnet.net.au (113.197.15.98)
                                                       160.020 ms 160.234 ms
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   138.44.5.1 (138.44.5.1) 160.124 ms 160.138 ms 160.068 ms
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   ombcr1-te-1-5.gw.unsw.edu.au (149.171.255.106)
                                                  160.090 ms 160.381 ms 160.185 ms
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   dcfw1-ae-1-3049.gw.unsw.edu.au (129.94.254.60)
                                                  160.592 ms 160.558 ms 160.949 ms
   www.engineering.unsw.edu.au (149.171.158.109)
                                                 160.978 ms 161.184 ms
                                                                         160.987 ms
```

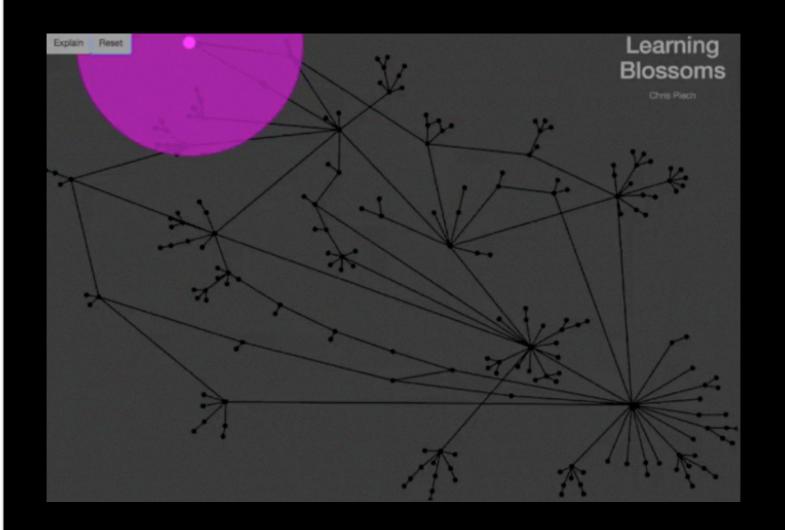
161 milliseconds to get to the final computer

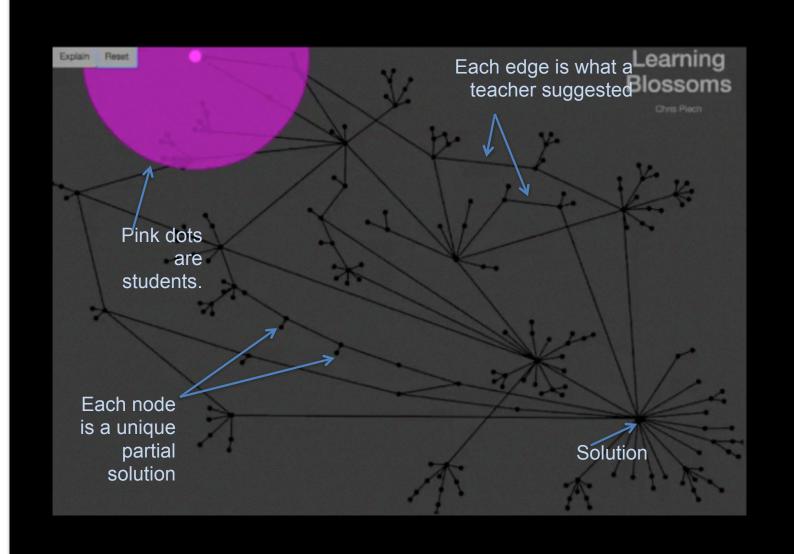


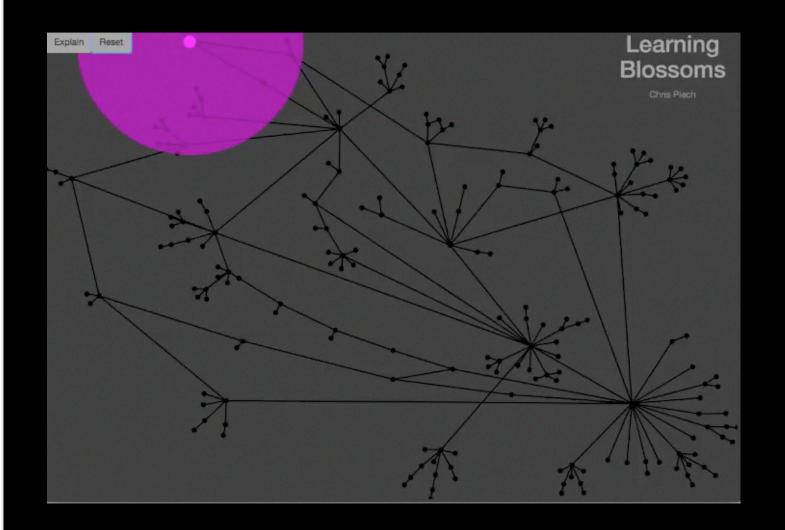
More Real Graphs: Chris Piech's Research!

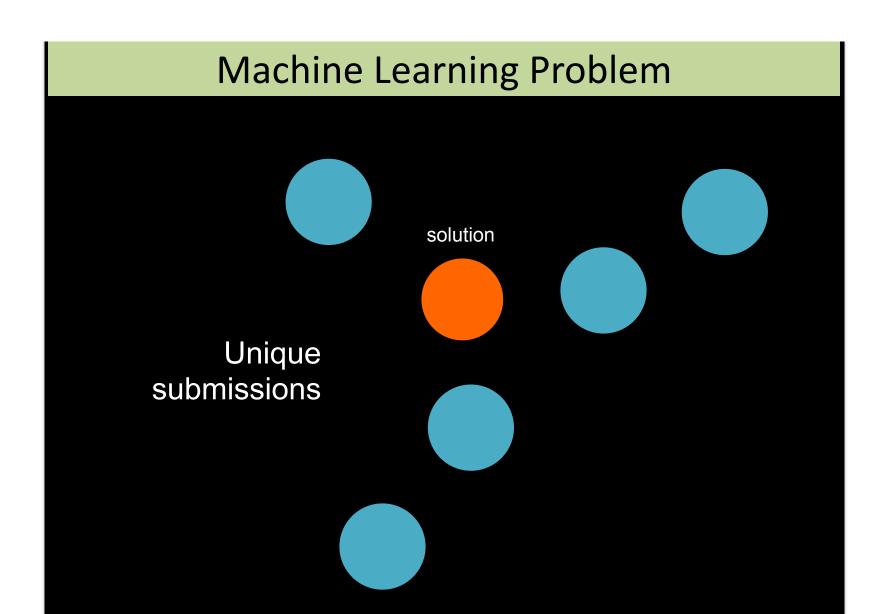




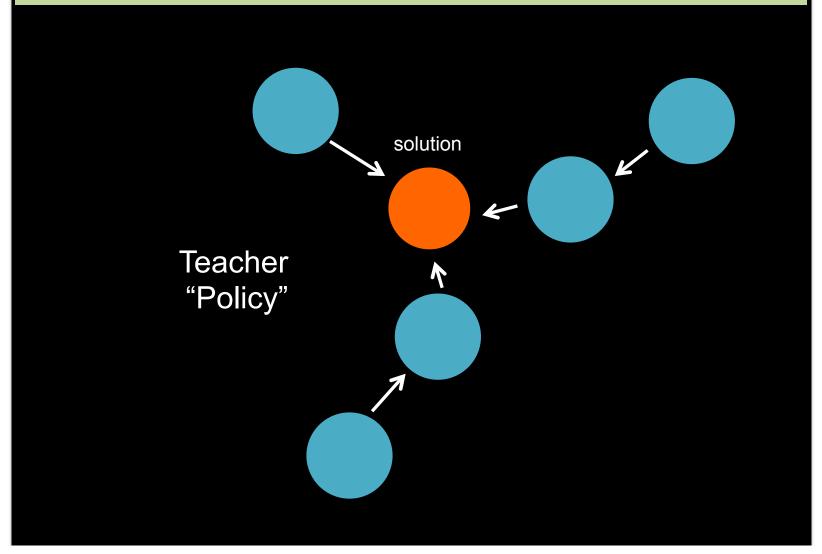






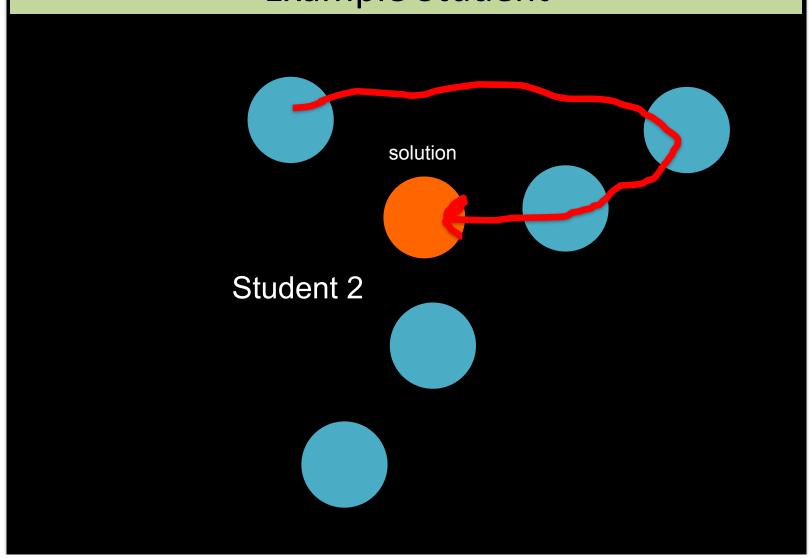


Machine Learning Problem



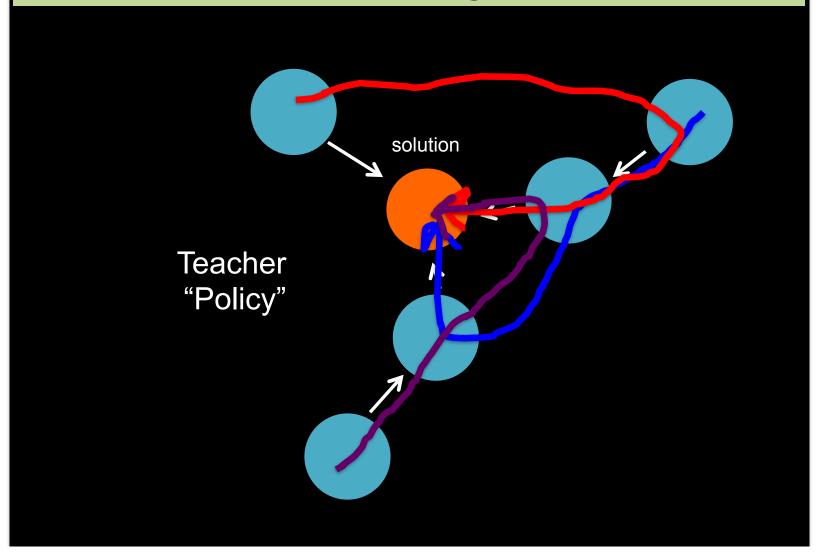
Example Student solution Student 1

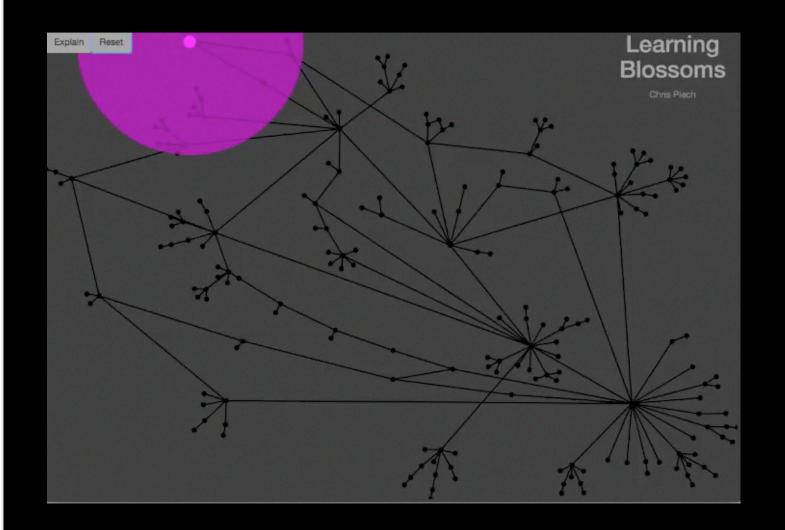
Example Student



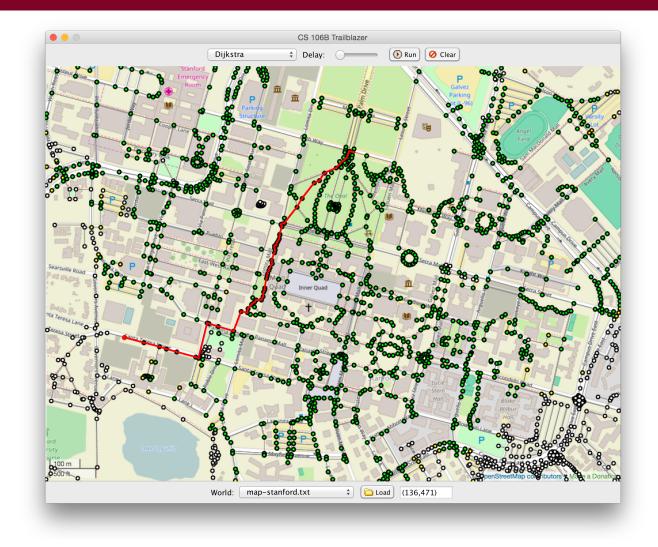
Example Student solution Student 1,203,403

Machine Learning Problem



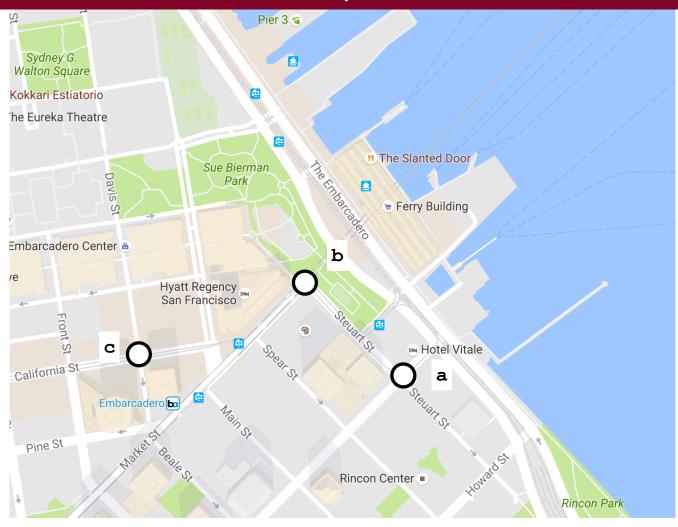


Back to Trailblazer

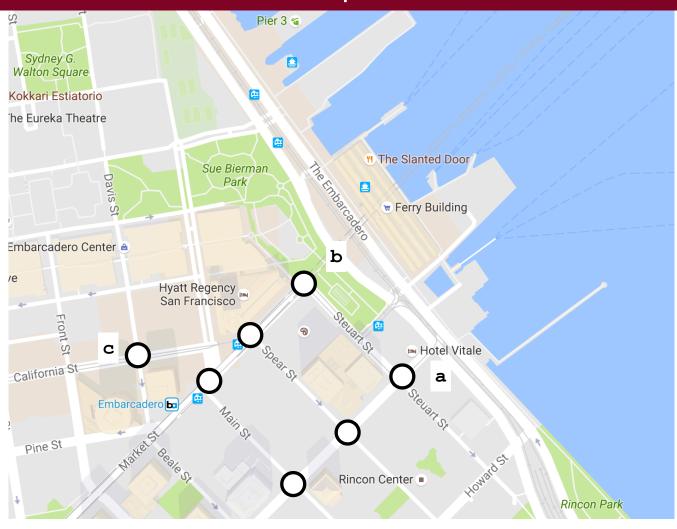




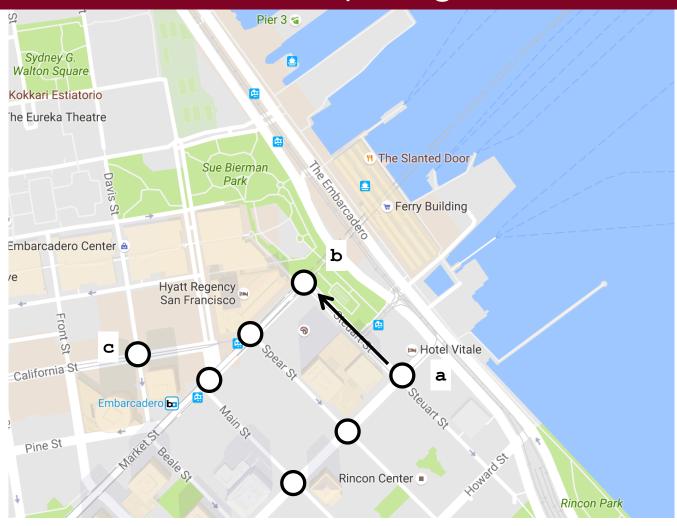
Road Map Node



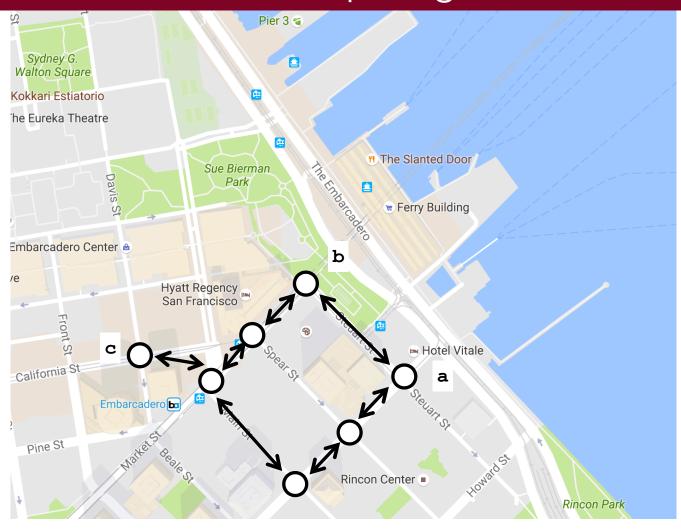
Road Map Node



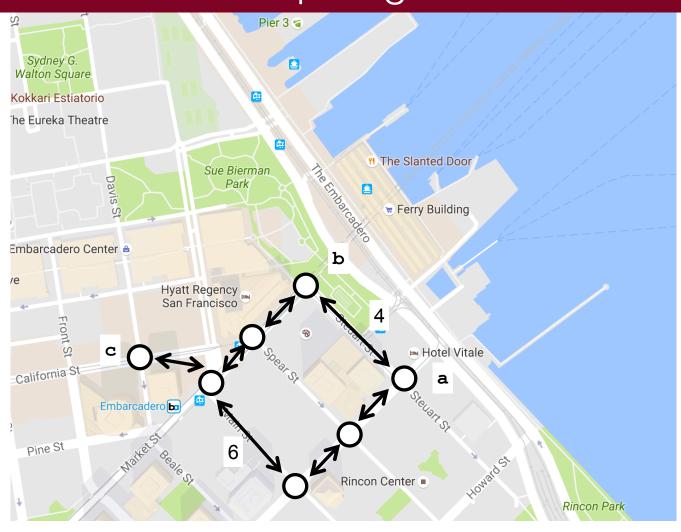
Road Map Edge



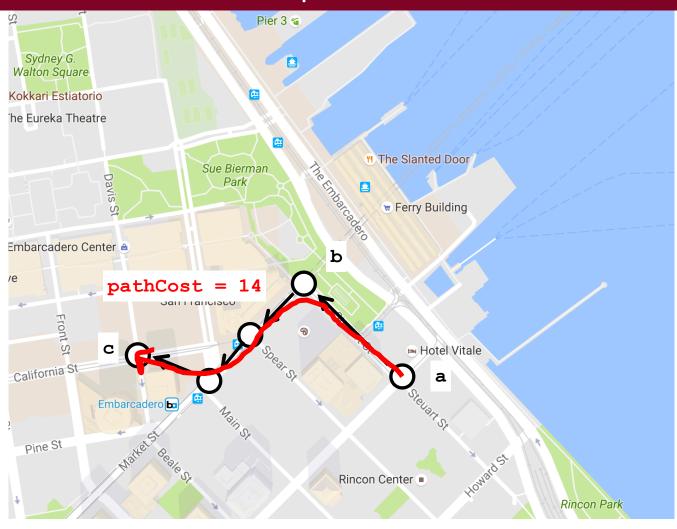
Road Map Edge

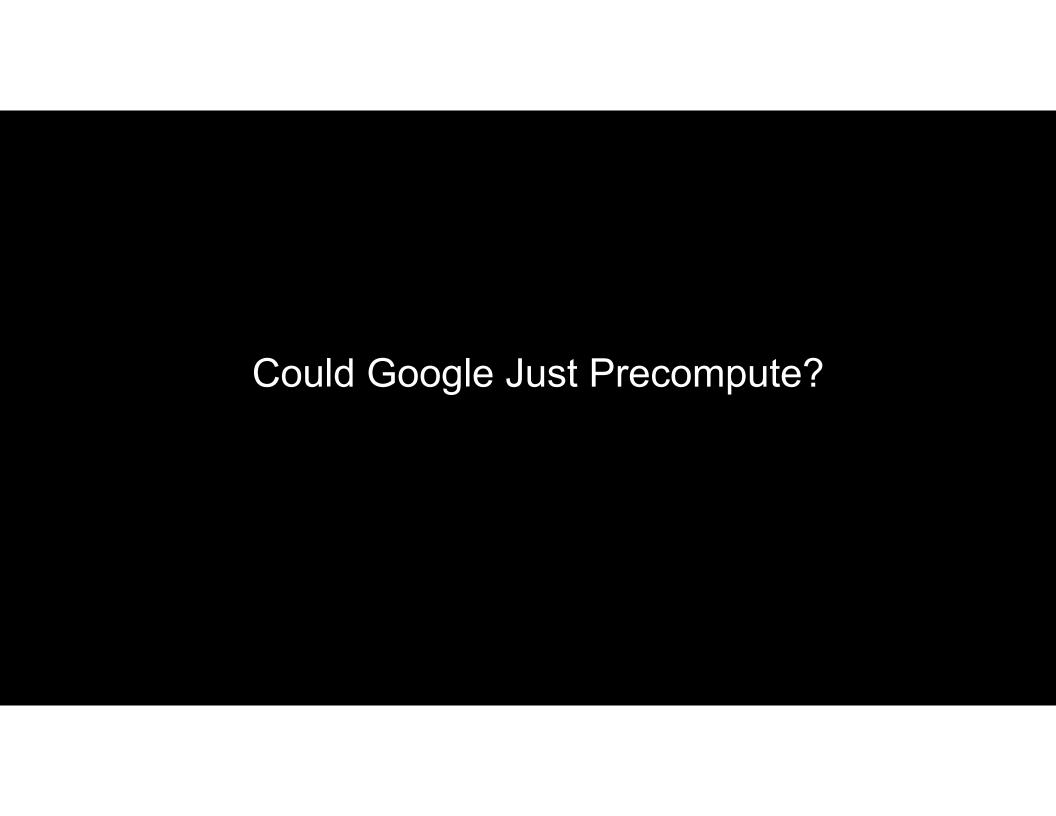


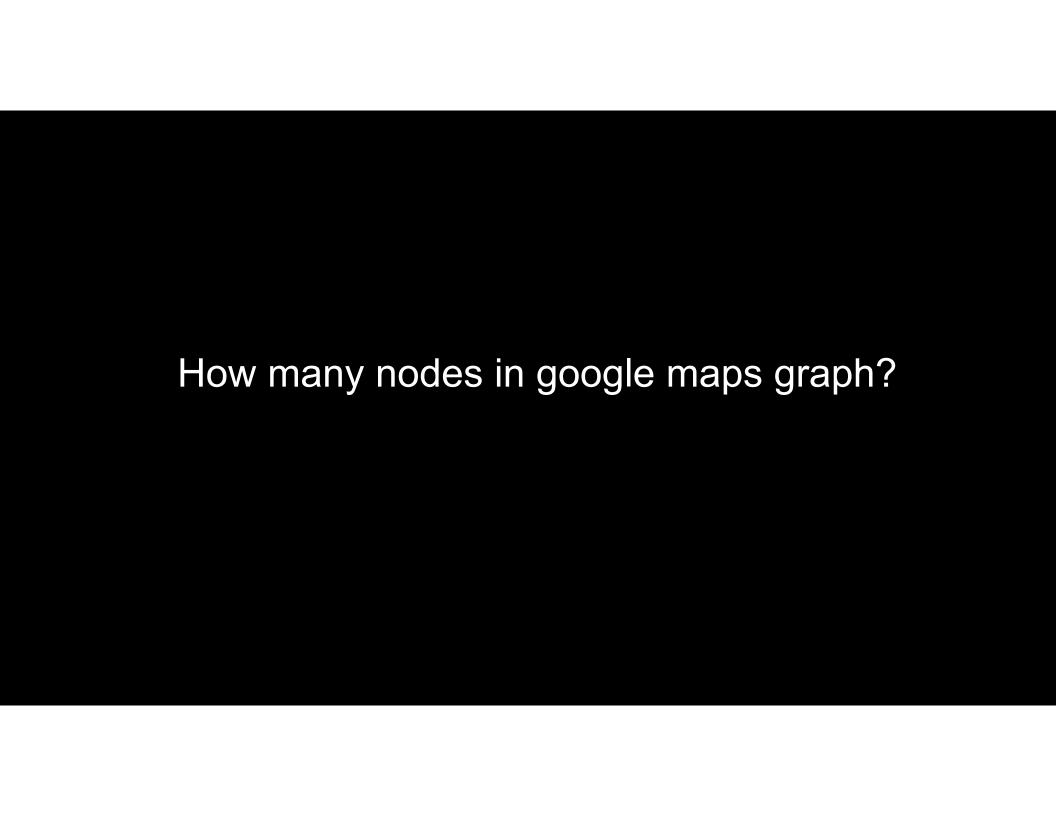
Road Map Edge Cost



Road Map Path Cost





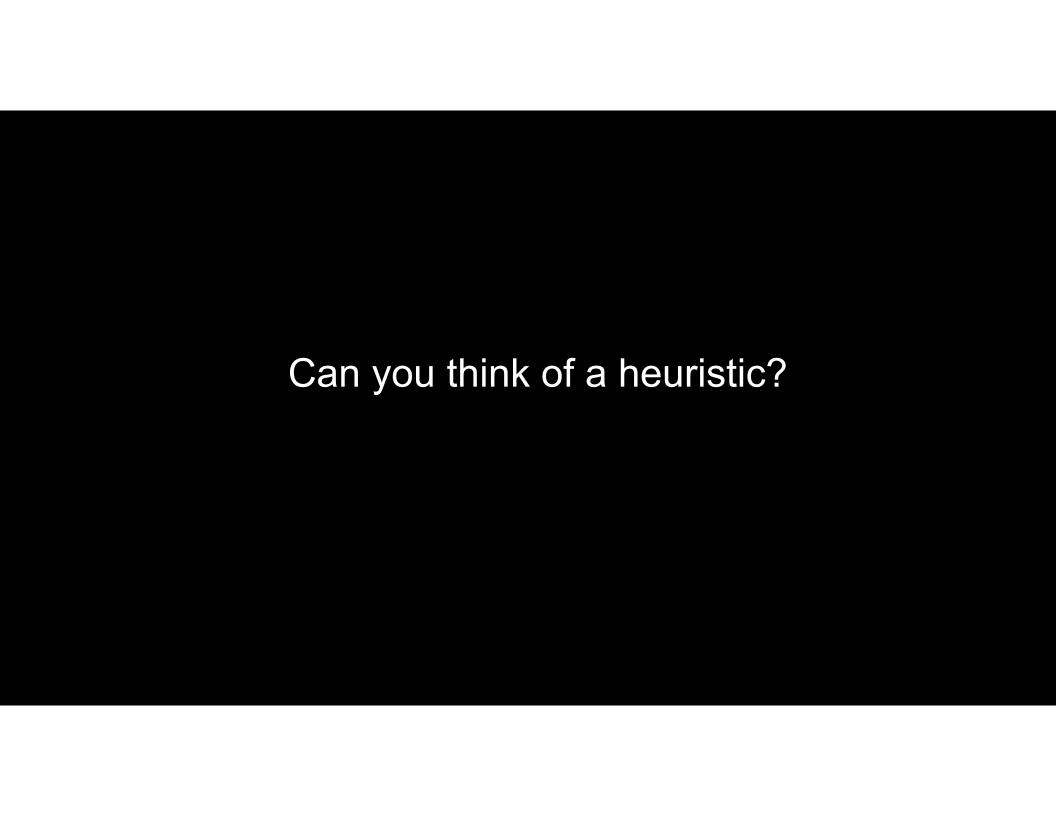




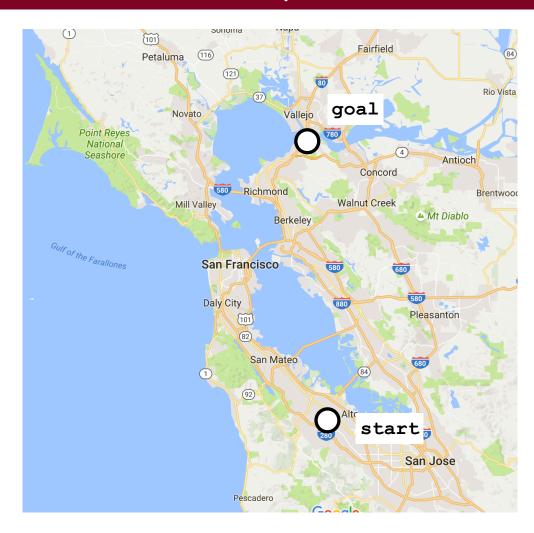


 6×10^{15}

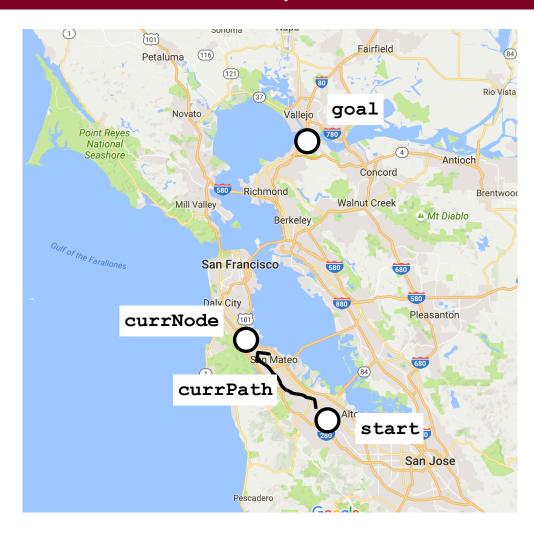
1 petasecond = 31.7 million years



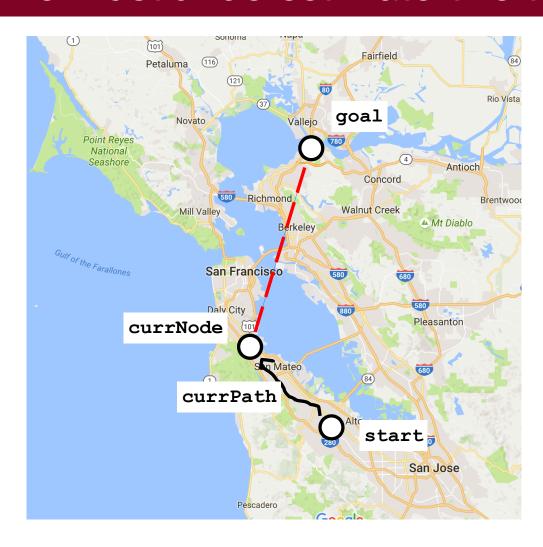
Road Map Heuristic



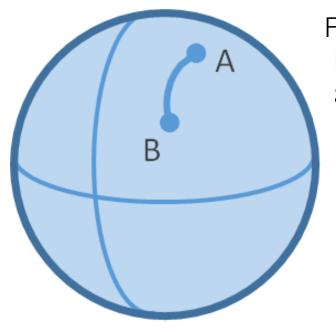
Road Map Heuristic



We must underestimate this time



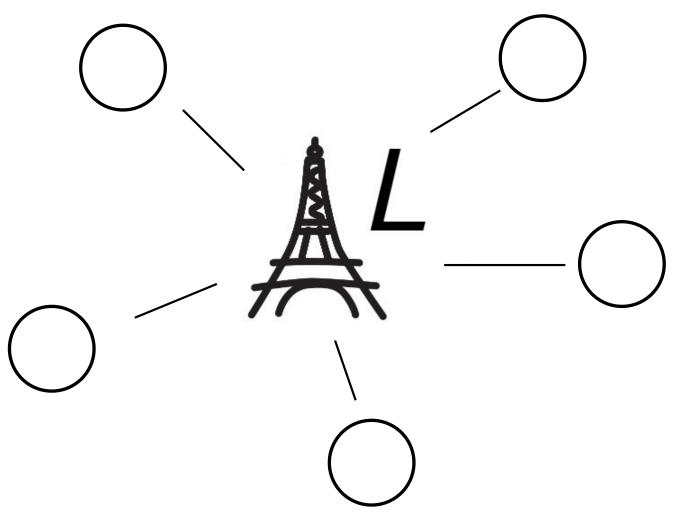
Direct Highway



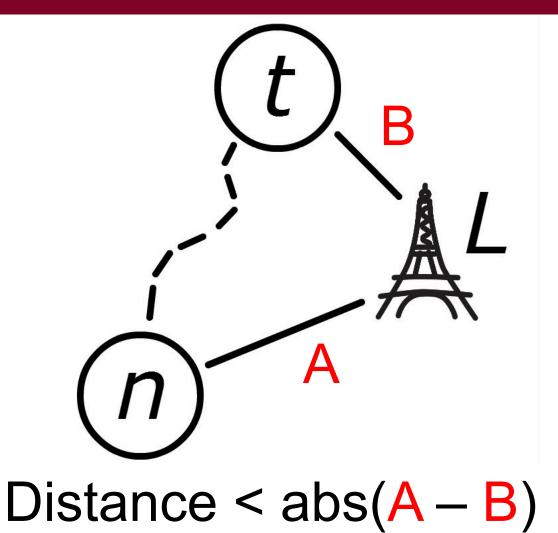
For Trailblazer:

Distance on surface of earth is **getCrowFlyDistance()**Speed on fastest highway is **getMaxRoadSpeed()**

Distance to Landmarks



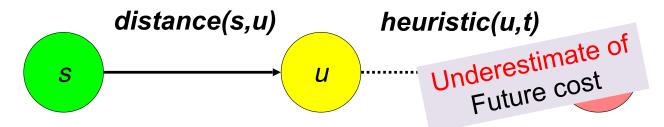
Landmark Heuristic



47

Best of All Heuristics

$$h = \max(h_1, h_2, ..., h_n)$$



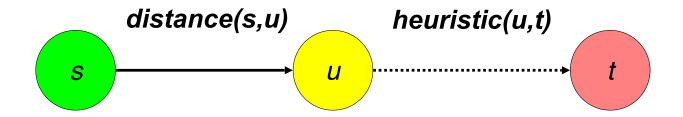
We want to underestimate the cost of our heuristic, by why? Let's look at the bounds of our choices:

heuristic(u,t) = 0

heuristic(u,t) = underestimate

heuristic(u,t) = perfect distance

heuristic(u,t) = overestimate



We want to underestimate the cost of our heuristic, by why? Let's look at the bounds of our choices:

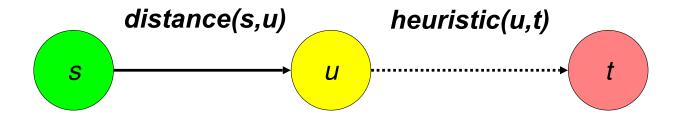
heuristic(u,t) = 0

heuristic(u,t) = underestimate

heuristic(u,t) = perfect distance

heuristic(u,t) = overestimate

Same as Dijkstra



We want to underestimate the cost of our heuristic, by why? Let's look at the bounds of our choices:

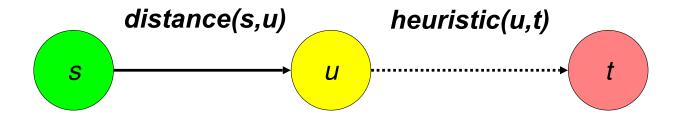
heuristic(u,t) = 0

heuristic(u,t) = underestimate

heuristic(u,t) = perfect distance

heuristic(u,t) = overestimate

Will be the same or faster than Dijkstra, and will find the shortest path (this is the only "admissible" heuristic for A*.



We want to underestimate the cost of our heuristic, by why? Let's look at the bounds of our choices:

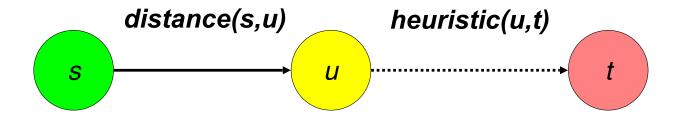
heuristic(u,t) = 0

heuristic(u,t) = underestimate

heuristic(u,t) = perfect distance

heuristic(u,t) = overestimate

Will only follow the best path, and will find the best path fastest (but requires perfect knowledge)



We want to underestimate the cost of our heuristic, by why? Let's look at the bounds of our choices:

heuristic(u,t) = 0

heuristic(u,t) = underestimate

heuristic(u,t) = perfect distance

heuristic(u,t) = overestimate

Won't necessarily find shortest path (but might run even faster)

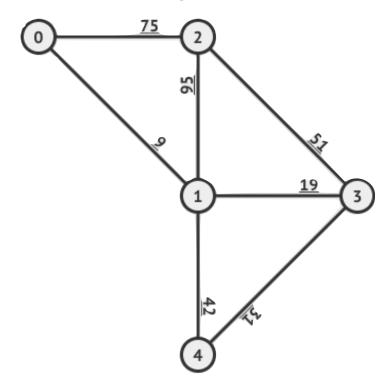
Admissible Heuristic

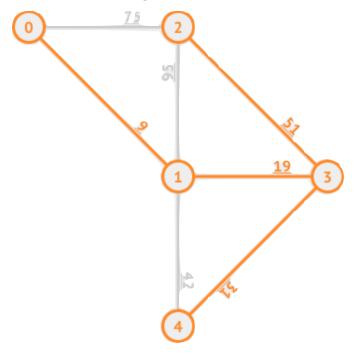
Definition: An admissible heuristic always underestimates the true cost.

Could you precompute this for all your vertices? Yes, but it would not be feasible.

Spanning Trees and Minimum Spanning Trees

Definition: A **Spanning Tree (ST)** of a connected undirected weighted graph **G** is a subgraph of **G** that is a **tree** and **connects (spans) all vertices of G**. A graph **G** can have multiple STs. A **Minimum Spanning Tree (MST)** of **G** is a ST of **G** that has the **smallest total weight** among the various STs. A graph **G** can have multiple MSTs but the MST weight is unique.





Minimum Spanning Tree

Kruskal's Algorithm to find a Minimum Spanning Tree

• Kruskal's algorithm: Finds a MST in a given graph.

function **kruskal**(graph):

Remove all edges from the graph.

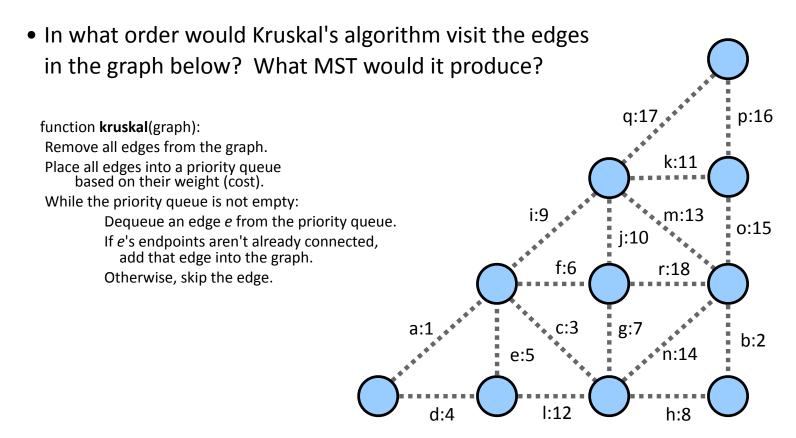
Place all edges into a priority queue based on their weight (cost).

While the priority queue is not empty:

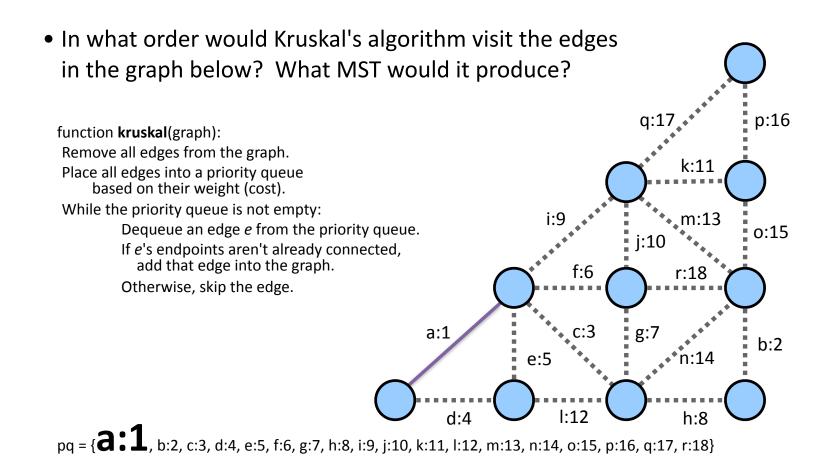
Dequeue an edge e from the priority queue.

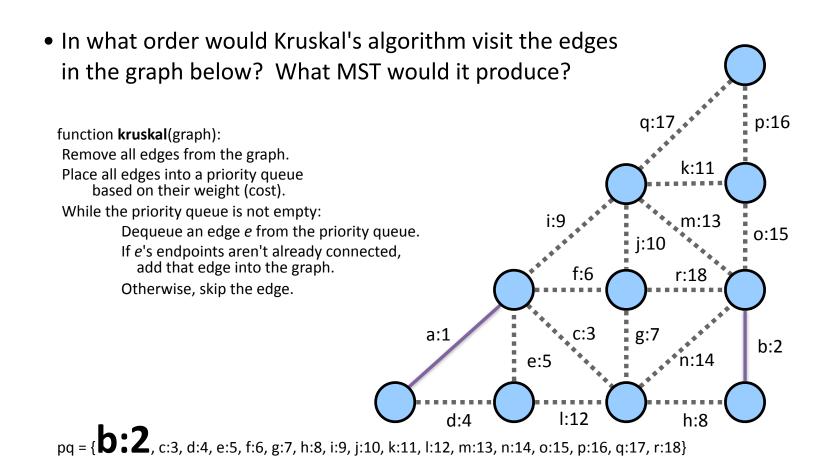
If e's endpoints aren't already connected to one another, add that edge into the graph.

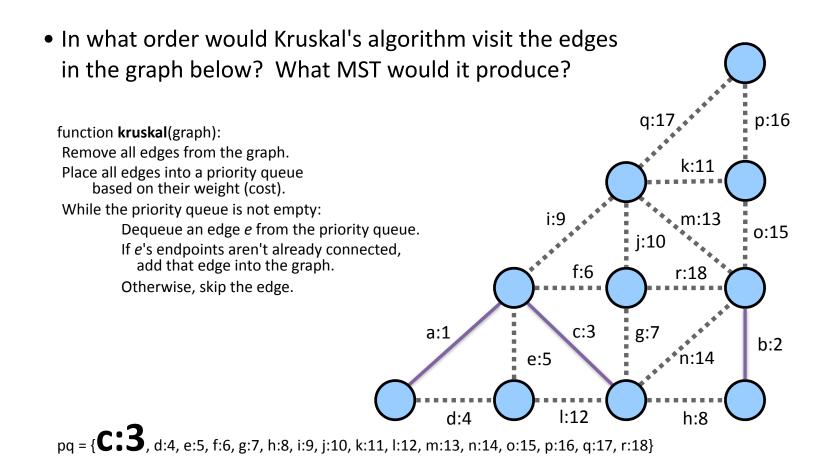
Otherwise, skip the edge.

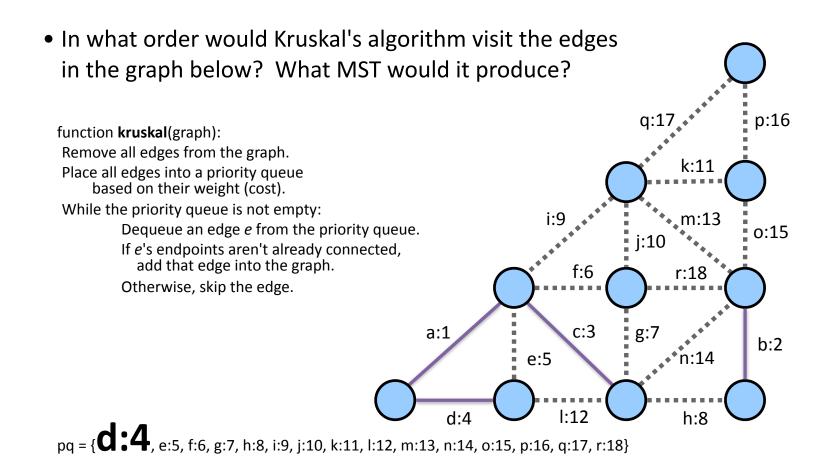


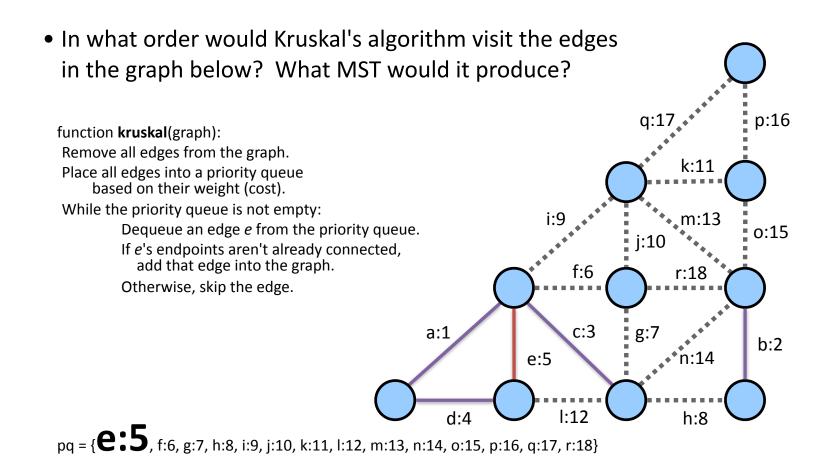
pq = {a:1, b:2, c:3, d:4, e:5, f:6, g:7, h:8, i:9, j:10, k:11, l:12, m:13, n:14, o:15, p:16, q:17, r:18}

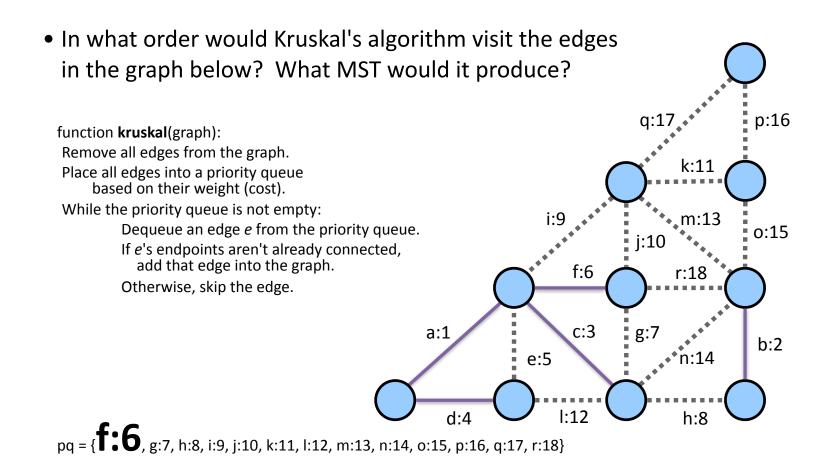


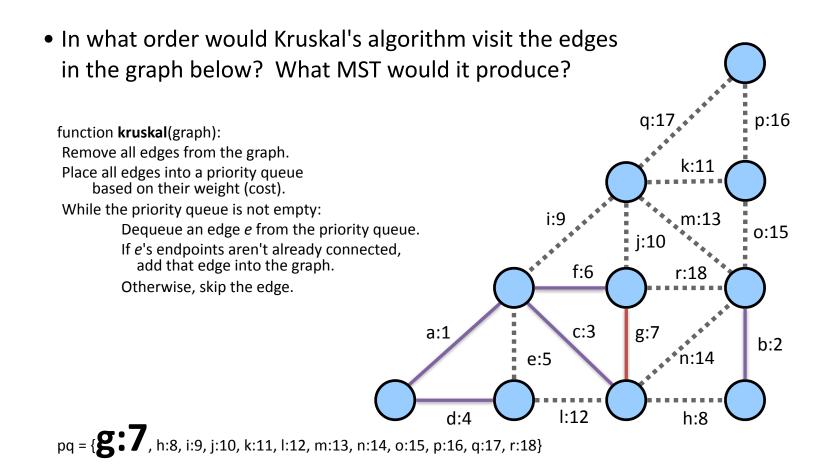


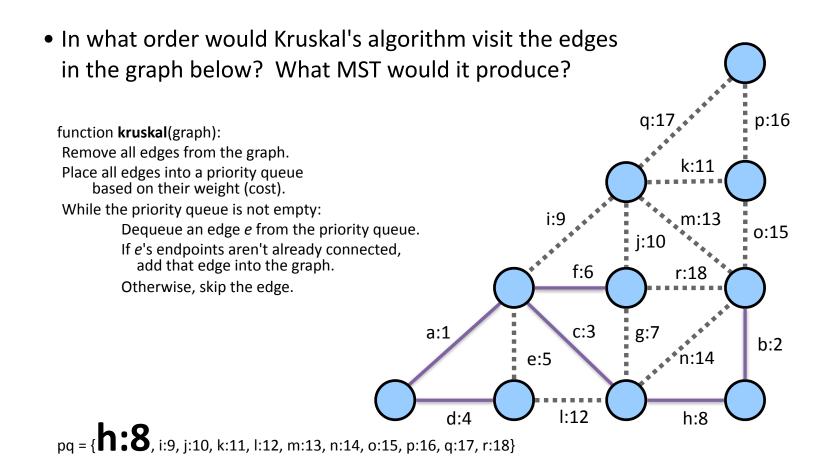


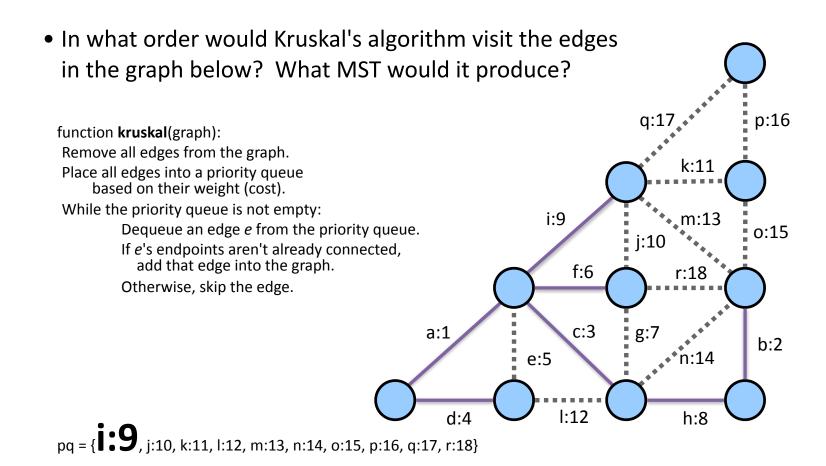


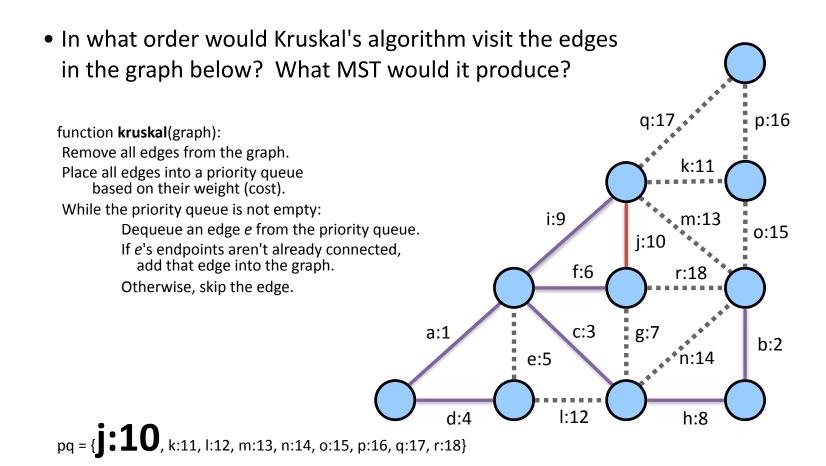


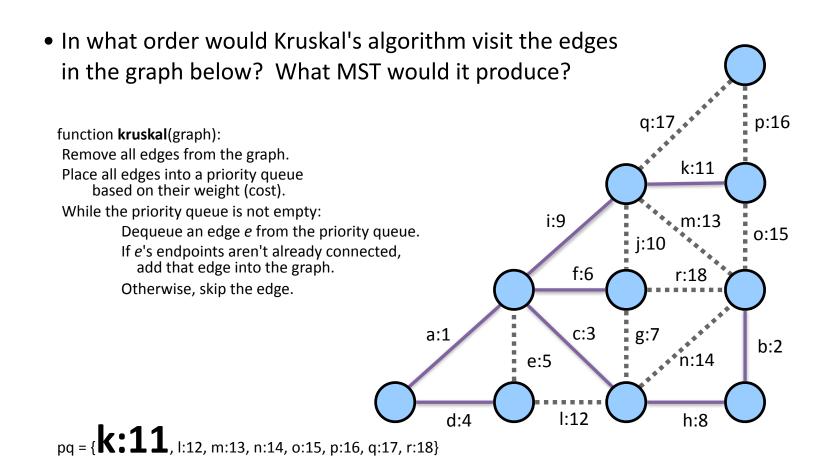


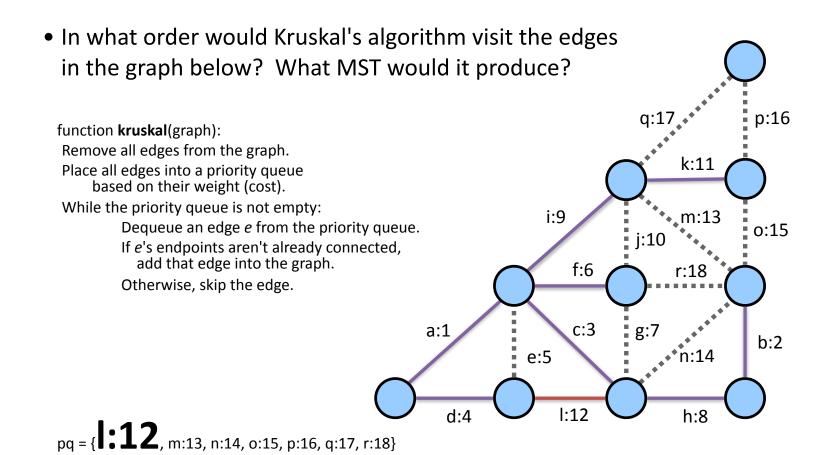


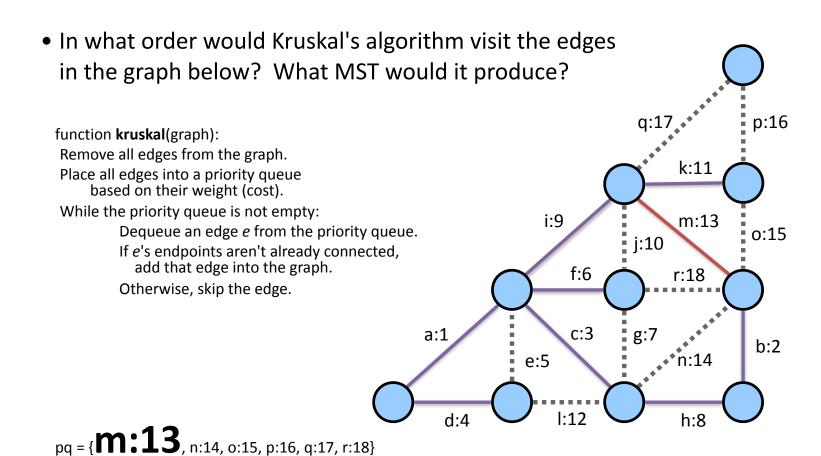


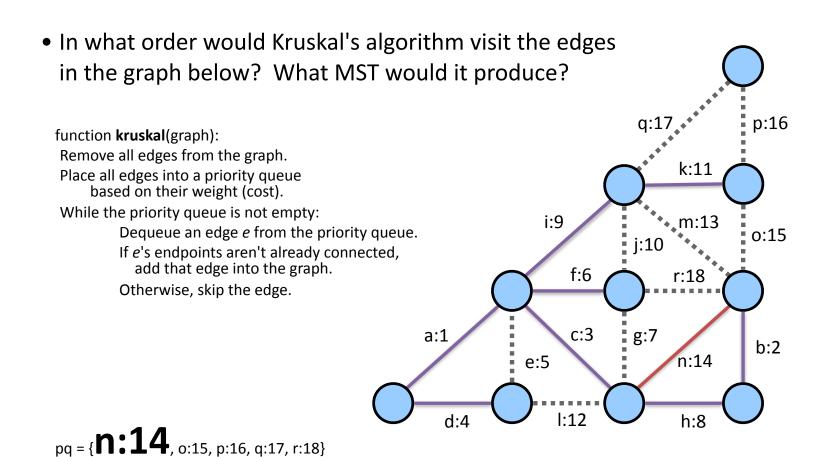


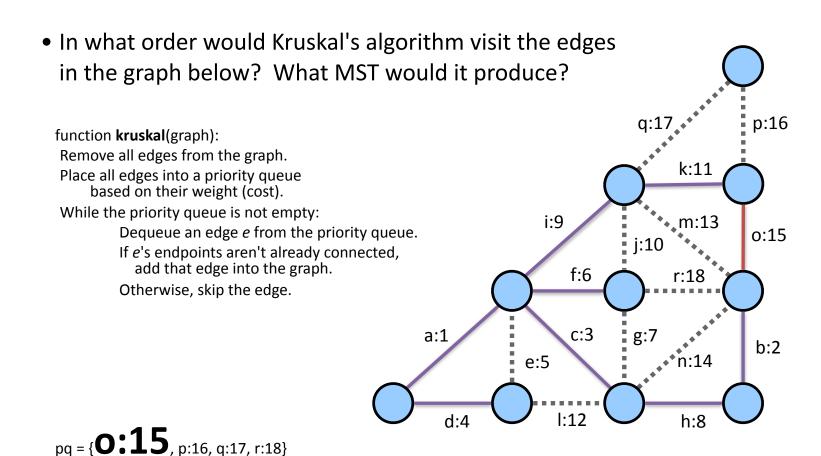


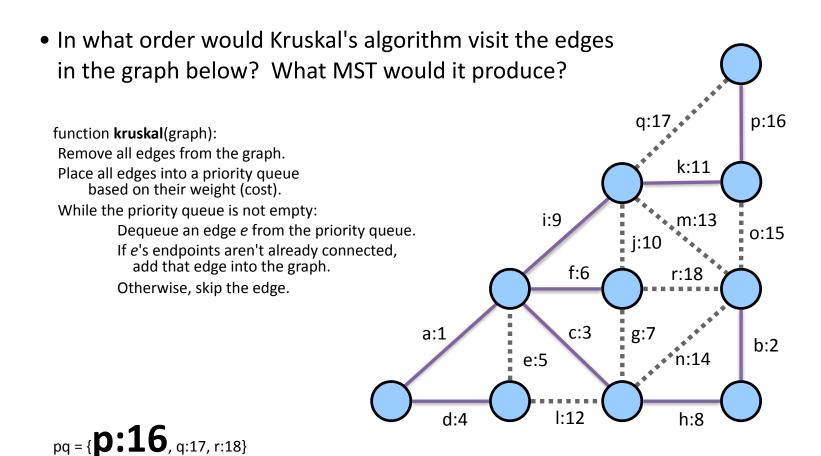


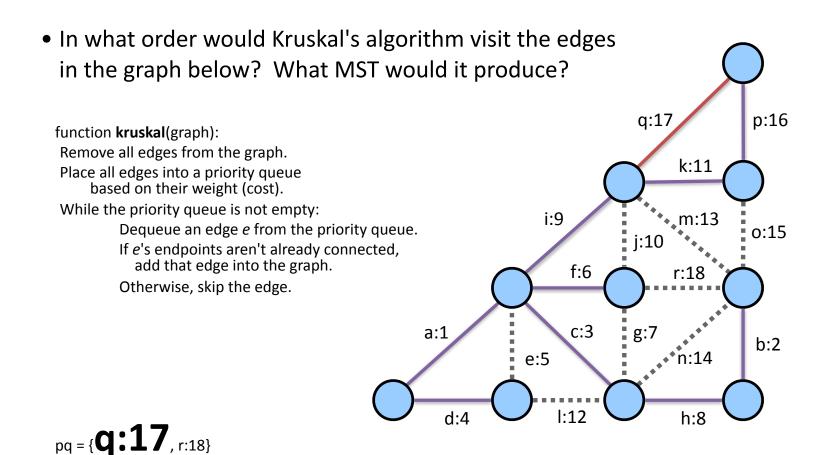


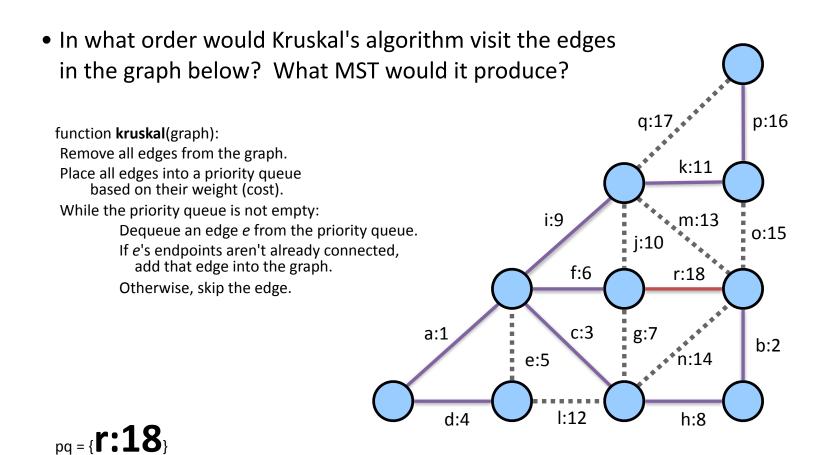












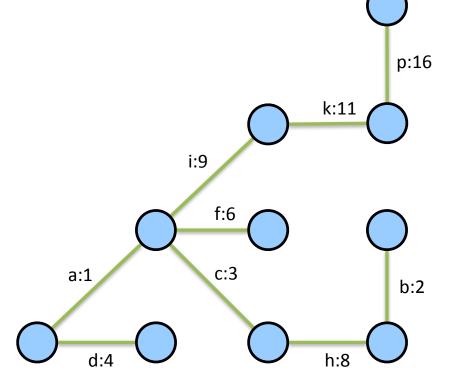
• In what order would Kruskal's algorithm visit the edges in the graph below? What MST would it produce? q:17 p:16 function **kruskal**(graph): Remove all edges from the graph. k:11 Place all edges into a priority queue based on their weight (cost). i:9 m:13 While the priority queue is not empty: o:15 j:10 Dequeue an edge *e* from the priority queue. If e's endpoints aren't already connected, f:6 r:18 add that edge into the graph. Otherwise, skip the edge. c:3 g:7 a:1 b:2 e:5 n:14

 $pq = \{\}$

• Kruskal's algorithm would output the following MST:

• The MST's total cost is:

$$1+2+3+4+6+8+9+11+16 = 60$$



What data structures should we use to implement this algorithm?

function **kruskal**(graph):

Remove all edges from the graph.

Place all edges into a **<u>priority queue</u>** based on their weight (cost).

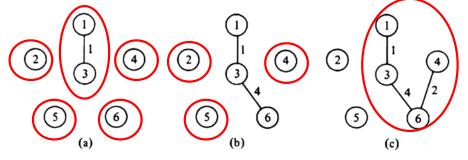
While the priority queue is not empty:

Dequeue an edge *e* from the priority queue.

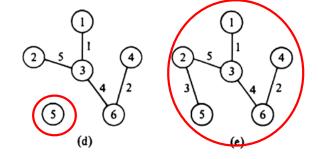
If e's endpoints aren't already connected, add that edge into the graph.

Otherwise, skip the edge.

- Need some way to identify which vertexes are "connected" to which other ones
 - we call these "clusters" of vertices
- Also need an efficient way to figure out which cluster a given vertex is in.



• Also need to **merge clusters** when adding an edge.



References and Advanced Reading

· References:

- •A* Heuristics: http://theory.stanford.edu/~amitp/GameProgramming/Heuristics.html
- •Minimum Spanning Tree visualization: https://visualgo.net/mst
- •Kruskal's Algorithm: https://en.wikipedia.org/wiki/Kruskal's_algorithm

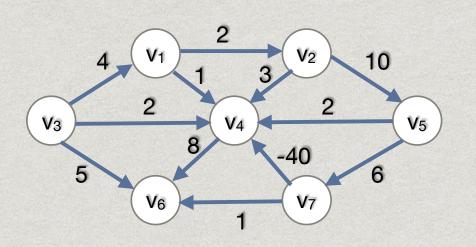
Advanced Reading:

- •How Internet Routing works: https://web.stanford.edu/class/msande91si/www-spr04/readings/week1/InternetWhitepaper.htm
- http://www.explainthatstuff.com/internet.html



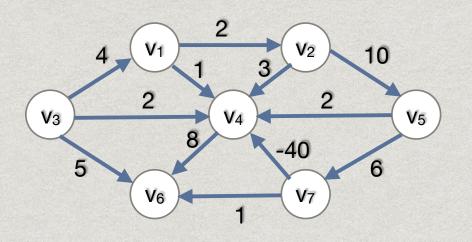
Extra Slides





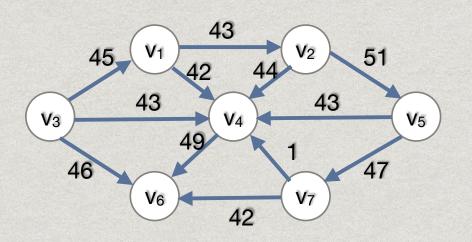
Dijkstra fails with negative edge costs. Once a vertex is declared known (say,v₄), it is possible from some other unknown vertex to create a shorter path to the vertex (say, by eventually looking at v₇→v₄).

Is there an easy solution?



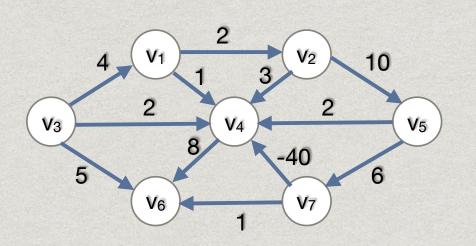
Is there an easy solution?

A naïve approach might be to add a delta (in this case, 41) to all paths, and then apply Dijkstra's algorithm, but this fails because paths with many edges become more weighty than paths with few edges.

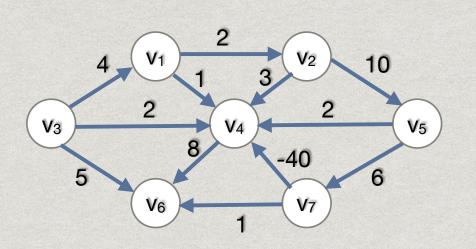


Is there an easy solution?

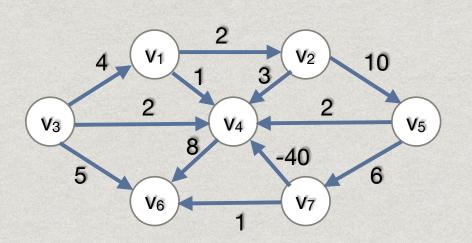
A naïve approach might be to add a delta (in this case, 41) to all paths, and then apply Dijkstra's algorithm, but this fails because paths with many edges become more weighty than paths with few edges.



So, there isn't a particularly easy solution. However, we can solve the problem with a combination of the weighted and unweighted algorithms, but at a drastically increased running time cost.

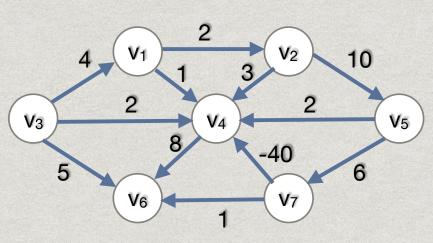


We have to forget about the idea of "known" vertices, since we will have to be able to change our mind if necessary (the greedy algorithm doesn't work properly).



Idea:

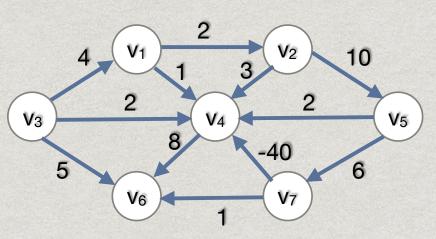
- 1. Place s on a queue
- 2. At each stage, dequeue a vertex, v. Then, find all vertices, w, adjacent to v, such that: $d_w > d_v + cost_{v,w}$
- 3. Update d_w and place w on the queue if it isn't already there (set a boolean to indicate presence in the queue)
- 4. Repeat until the queue is empty.



V	in queue?	d _v	p _v
V1	Yes	0	0
V ₂	No	INF	0
V3	No	INF	0
V4	No	INF	0
V5	No	INF	0
V ₆	No	INF	0
V7	No	INF	0

queue V₁

Dequeue v_1 and check weights for v_2 and v_4 . Update and place in queue.

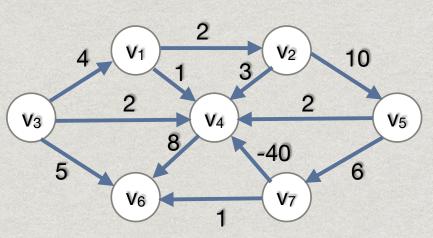


V	in queue?	d _v	p _v
V1	No	0	0
V ₂	Yes	2	V ₁
V3	No	INF	0
V4	Yes	1	V ₁
V5	No	INF	0
V ₆	No	INF	0
V7	No	INF	0

queue



Dequeue v_2 and check weights for v_4 and v_5 . Update and place in queue.

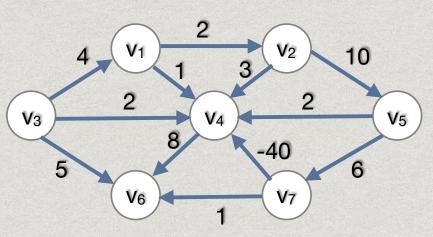


V	in queue?	d _v	pv
V1	No	0	0
V ₂	No	2	V ₁
V3	No	INF	0
V4	Yes	1	V ₁
V5	Yes	12	V2
V ₆	No	INF	0
V7	No	INF	0

queue

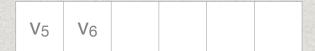
	V4	V5				
--	----	----	--	--	--	--

Dequeue v₄ and check weight for v₆. Update and place in queue.

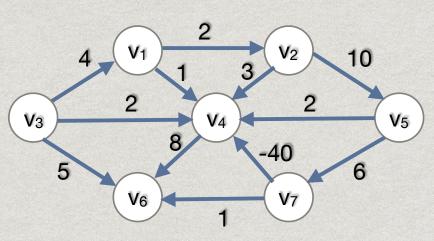


V	in queue?	d _v	p _v
V1	No	0	0
V ₂	No	2	V ₁
V3	No	INF	0
V4	No	1	V ₁
V5	Yes	12	V2
V ₆	Yes	9	V4
V7	No	INF	0

queue



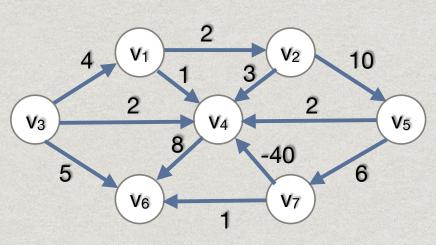
Dequeue v_5 and check weights for v_4 and v_7 . Update and place in queue.



V	in queue?	d _v	p _v
V1	No	0	0
V ₂	No	2	V ₁
V3	No	INF	0
V4	No	1	V ₁
V5	No	12	V2
V ₆	Yes	9	V4
V7	Yes	18	V5

queue

Dequeue v₆ and there aren't any weights to check (v₆ doesn't have any out-going edges).

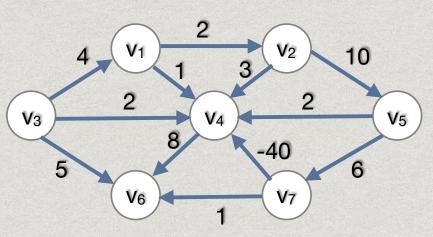


200				
	V	in queue?	d _v	p _v
	V1	No	0	0
	V ₂	No	2	V ₁
	V3	No	INF	0
	V4	No	1	V7
	V5	No	12	V2
	V ₆	No	9	V4
	V7	Yes	18	V5

queue



Dequeue v₇ and check weights for v₄ and v₆. v₄ will get updated and placed back in the queue.

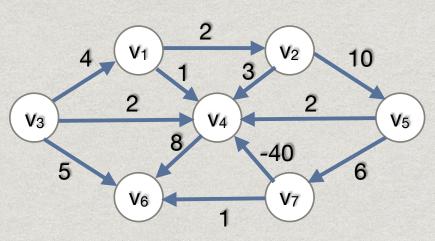


V	in queue?	d _v	p _v
V1	No	0	0
V ₂	No	2	V ₁
V3	No	INF	0
V4	Yes	-22	V7
V5	No	12	V ₂
V ₆	No	9	V4
V7	No	18	V5

queue



Dequeue v₄ and check weight for v₆. v₆ will get updated and placed back in the queue.

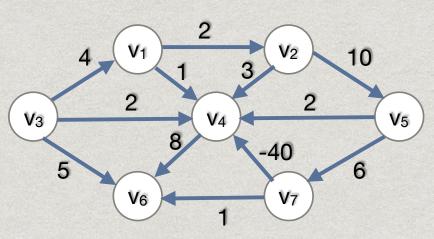


V	in queue?	d _v	p _v
V1	No	0	0
V ₂	No	2	V ₁
V3	No	INF	0
V4	No	-22	V7
V5	No	12	V2
V ₆	Yes	-14	V4
V7	No	18	V5

queue



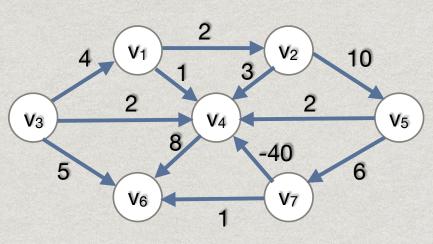
Finally, Dequeue v₆. There aren't any vertices to check (no out-going edges from v₆), and that will empty the queue.



V	in queue?	d _v	p _v
V1	No	0	0
V ₂	No	2	V ₁
V3	No	INF	0
V4	No	-22	V7
V5	No	12	V2
V ₆	No	-14	V4
V7	No	18	V5



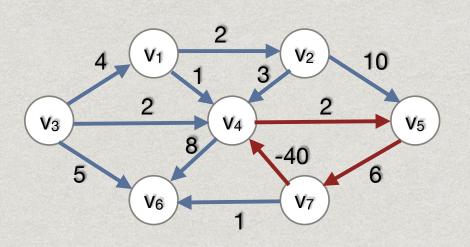
Finally, Dequeue v₆. There aren't any vertices to check (no out-going edges from v₆), and that will empty the queue.



V	in queue?	d _v	p _v
V1	No	0	0
V ₂	No	2	V ₁
V3	No	INF	0
V4	No	-22	V7
V5	No	12	V2
V ₆	No	-14	V4
V7	No	18	V5

Running time?

Each vertex can be dequeued at most |V| times per edge, meaning that the running time is now O(|E| * |V|), which is significantly worse than for the algorithm without negative costs.



What about negative cost cycles?

This will run our queue indefinitely, so we need to make a decision about when to stop.

If you stop after every vertex has been dequeued |V|+1 times, you will guarantee termination.