

Integer Representation

Announcements

assign0 due tonight

assign1 out tomorrow

Labs start this week

SCPD

Note on office hours on Piazza

Will get an email tonight about labs

Goals for Today

Introduction to computer arithmetic

Binary addition and subtraction

Overflow

Represent negative numbers

Compare/contrast unsigned and signed integers

Add two 1-bit numbers

a	b	a + b
0	0	00
0	1	01
1	0	01
1	1	10

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LSB looks like XOR, next bit looks like AND

$$a + b = ((a \& b) \ll 1) | (a \wedge b)$$

(Only when a and b are 1-bit)

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LSB looks like XOR, next bit looks like AND

$$a + b = ((a \& b) \ll 1) | (a \wedge b)$$

(Only when a and b are 1-bit)

This is called a half adder

"Full" addition requires us to handle carries

Addition in Binary

$$\begin{array}{r} 1 \\ 107 \\ + 58 \\ \hline 165 \end{array}$$

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$$\begin{array}{r} 1 \quad 1 \\ 0110 \quad 1011 \\ + 0011 \quad 1010 \\ \hline 0101 \end{array}$$

Addition in Binary

$$\begin{array}{r} 1 \\ 107 \\ + 58 \\ \hline 165 \end{array} \quad \begin{array}{r} 1111 \quad 1 \\ 0110 \quad 1011 \\ + 0011 \quad 1010 \\ \hline 1010 \quad 0101 \end{array}$$

Same as decimal addition, just a lot more carrying

Overflow

$$\begin{array}{r} 255 \\ + 1 \\ \hline \end{array} \quad \begin{array}{r} 1111 \ 1111 \\ + 0000 \ 0001 \\ \hline \end{array}$$

What happens when we run out of bits?

Overflow

$$\begin{array}{r} 255 \\ + 1 \\ \hline \end{array} \quad \begin{array}{r} 1 \\ 1111 \ 1111 \\ + 0000 \ 0001 \\ \hline 0 \end{array}$$

What happens when we run out of bits?

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$$\begin{array}{r} 255 \\ + 1 \\ \hline \end{array} \quad \begin{array}{r} 1 \ 1111 \ 111 \\ 1111 \ 1111 \\ + 0000 \ 0001 \\ \hline 1 \ 0000 \ 0000 \end{array}$$

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$$\begin{array}{r} 255 \\ + 1 \\ \hline 0 \end{array} \quad \begin{array}{r} 1111 \ 111 \\ 1111 \ 1111 \\ + 0000 \ 0001 \\ \hline 0000 \ 0000 \end{array}$$

What happens when we run out of bits?

Overflow: extra bit is dropped

No warning, no indication

Overflow

$$\begin{array}{r} 255 \\ + 1 \\ \hline 0 \end{array} \quad \begin{array}{r} 1111 \ 111 \\ 1111 \ 1111 \\ + 0000 \ 0001 \\ \hline 0000 \ 0000 \end{array}$$

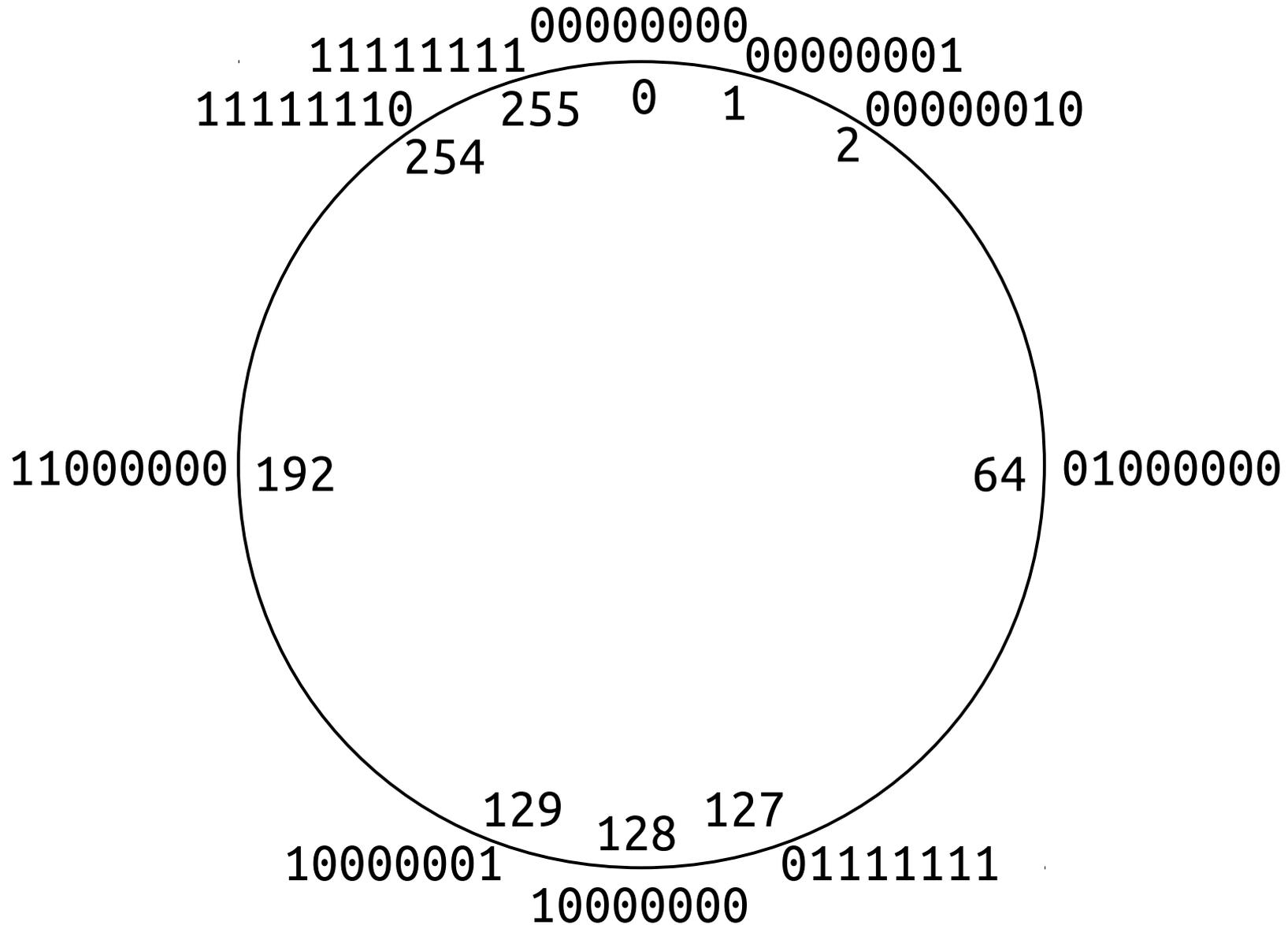
Modular arithmetic

Overflow leads to wrapping around, mod by 256

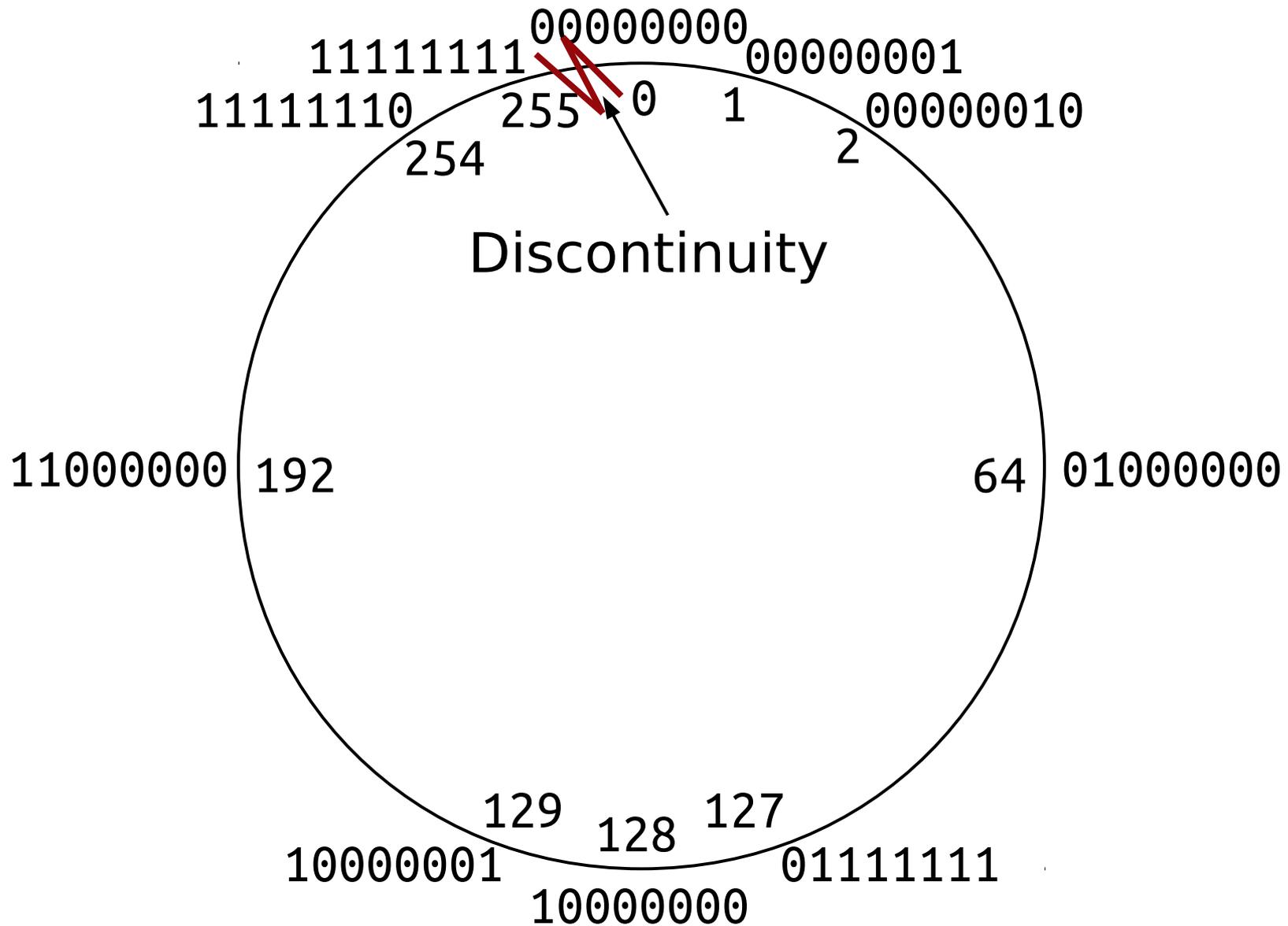
$$0x100 \% 256 = (1 \ 0000 \ 0000) \% 256 = 0$$

$$256 = 2^8$$

unsigned char Number Circle



unsigned char Number Circle



What about subtraction?

Could use same approach as addition

Start with 1-bit numbers, then deal with borrowing

What about subtraction?

Could use same approach as addition

Start with 1-bit numbers, then deal with borrowing

But we don't like inventing new hardware

Idea: Subtraction is just adding the negative

$a - b$ becomes $a + (-b)$

New problem: How do we represent negatives?

Deriving -1

$$\begin{array}{r} 1 \\ + \quad -1 \\ \hline 0 \end{array}$$

Deriving -1

$$\begin{array}{r} 1 \\ + -1 \\ \hline 0 \end{array} \quad \begin{array}{r} 0000 \ 0001 \\ + \color{red}{????} \ \color{red}{????} \\ \hline 0000 \ 0000 \end{array}$$

Let's backsolve for -1

We know that $1 + (-1) = 0$

Deriving -1

$$\begin{array}{r} 1 \\ + -1 \\ \hline 0 \end{array} \quad \begin{array}{r} 0000 \\ + \color{red}??? \\ \hline 0000 \end{array} \quad \begin{array}{r} 11 \\ 0001 \\ + \color{red}??11 \\ \hline 0000 \end{array}$$

Deriving -1

$$\begin{array}{r} 1 \\ + 1 \\ \hline 0 \end{array} \qquad \begin{array}{r} 1 \\ 1111 \\ + 1111 \\ \hline 0000 \\ 0000 \end{array}$$

Deriving -1

$$\begin{array}{r} 1 \\ + -1 \\ \hline 0 \end{array} \quad \begin{array}{r} 1 \\ + 1111 \\ \hline 1 \end{array}$$

Did it...work?

What about that extra 1?

Deriving -1

$$\begin{array}{r} 1 \\ + -1 \\ \hline 0 \end{array} \quad \begin{array}{r} 1111 111 \\ 0000 0001 \\ + 1111 1111 \\ \hline 0000 0000 \end{array}$$

Did it...work?

What about that extra 1?

Arithmetic is % 256, so it's dropped

What have we learned?

For 1-byte numbers

-1 is `0xff` = 1111 1111 (bin)

Note: same bit pattern as 255 in unsigned

`0 = 0x100 % 256 = 1 0000 0000 (bin) % 256`

But how do we generalize for other negatives?

Arithmetic Negation

Let's try something different...

Let n be a 1-byte number

$$-n = 0 - n$$

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$$\begin{aligned} -n &= 0 - n \\ &= 0x100 - n \end{aligned}$$

Reminder: $0x100 = 256 = 1\ 0000\ 0000$ (bin)

Arithmetic Negation

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Let n be a 1-byte number

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Reminder: $0xff = 255 = 1111\ 1111$ (bin)

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$$\begin{aligned} -n &= 0 - n \\ &= 0x100 - n \\ &= (0xff + 1) - n \\ &= (0xff - n) + 1 \end{aligned}$$

Wouldn't it be awesome if there were a simple way to get $0xff - n$?

0xff - n

$$\begin{array}{r} 0xff \\ - 0x35 \\ \hline \end{array} \quad \begin{array}{r} 1111 \ 1111 \\ - 0011 \ 0101 \\ \hline \end{array}$$

0xff - n

$$\begin{array}{r} 0xff \\ - 0x35 \\ \hline \end{array} \quad \begin{array}{r} 1111 \ 1111 \\ - 0011 \ 0101 \\ \hline 10 \end{array}$$

0xff - n

$$\begin{array}{r} 0xff \\ - 0x35 \\ \hline 0xca \end{array} \quad \begin{array}{r} 1111 \ 1111 \\ - 0011 \ 0101 \\ \hline 1100 \ 1010 \end{array}$$

There's always a 1 to take, never need to borrow

0xff - n

$$\begin{array}{r} 0xff \\ - 0x35 \\ \hline 0xca \end{array} \quad \begin{array}{r} 1111 \ 1111 \\ - 0011 \ 0101 \\ \hline 1100 \ 1010 \end{array}$$

$$0xff - n = \sim n$$

For 1-byte n

Arithmetic Negation

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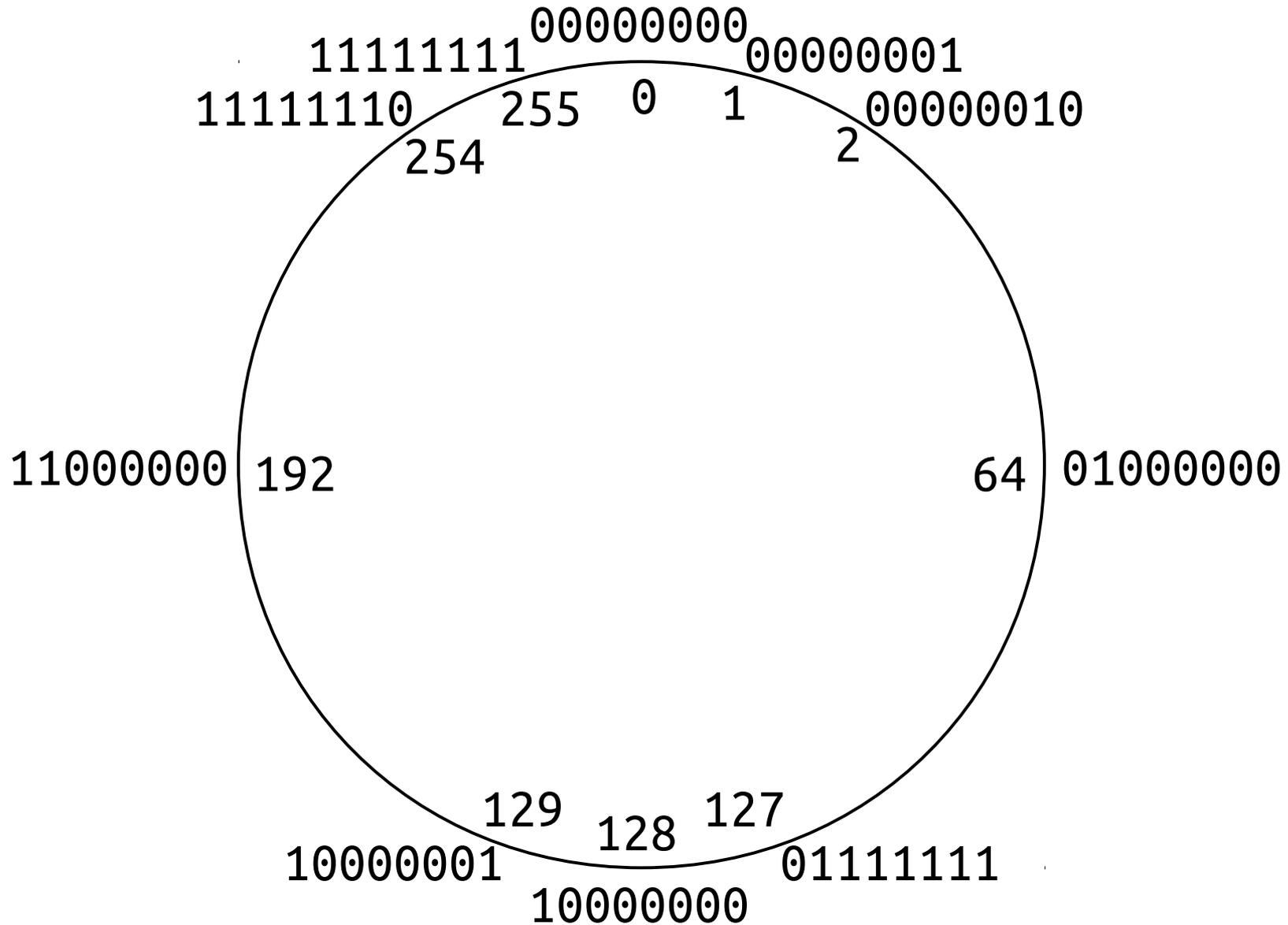
$$\mathbf{-n = \sim n + 1}$$

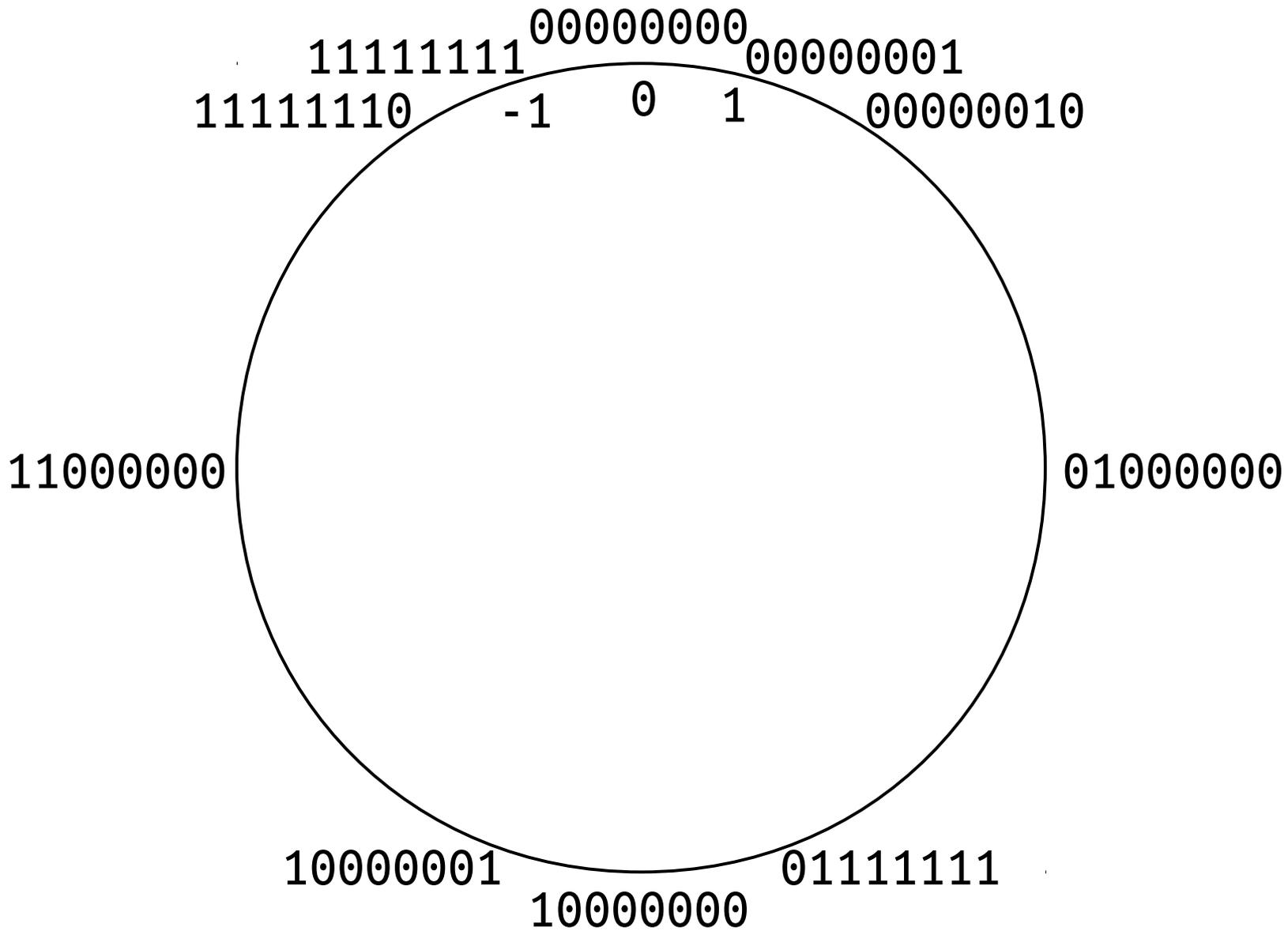
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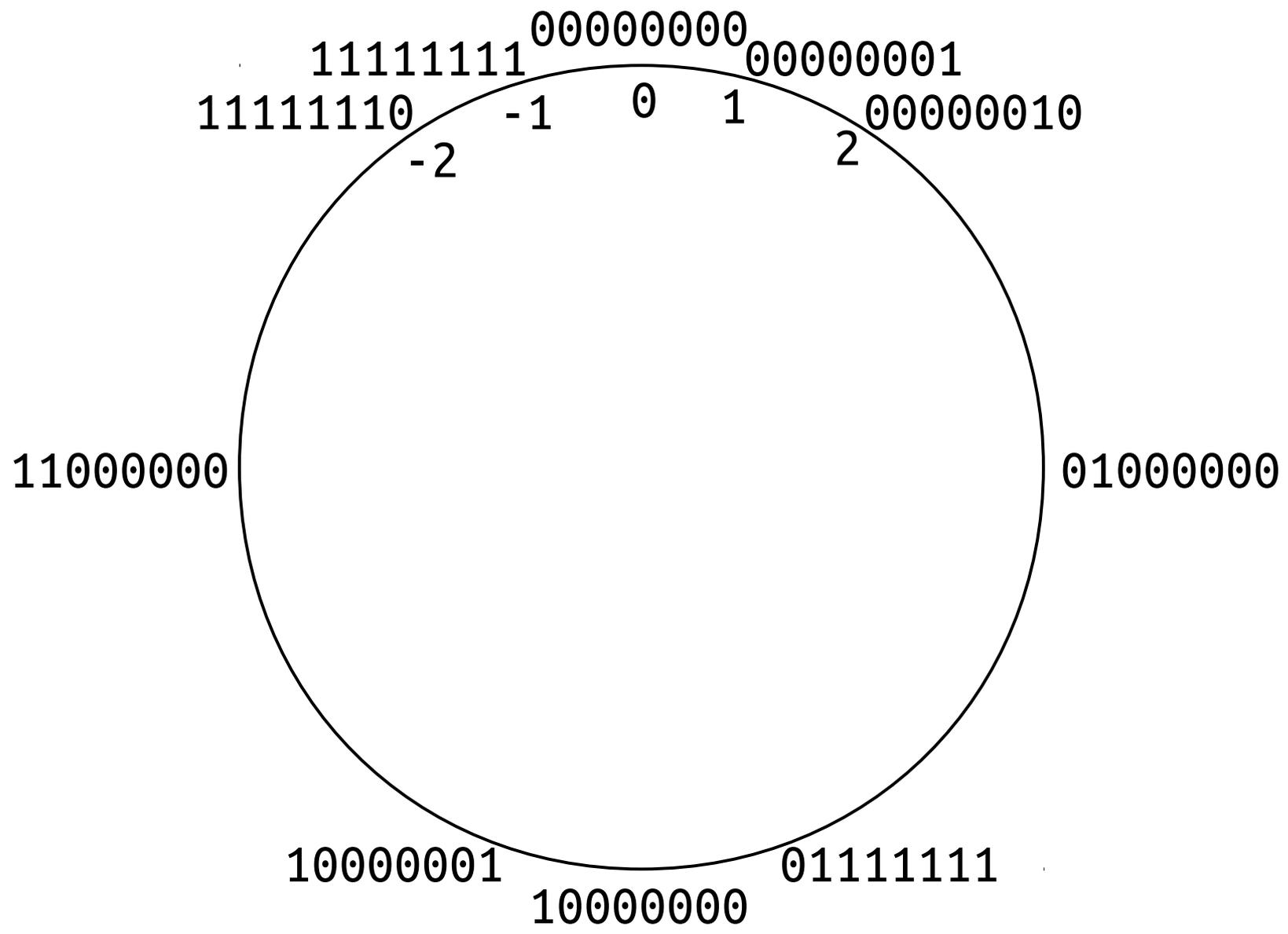
This is called 2's complement

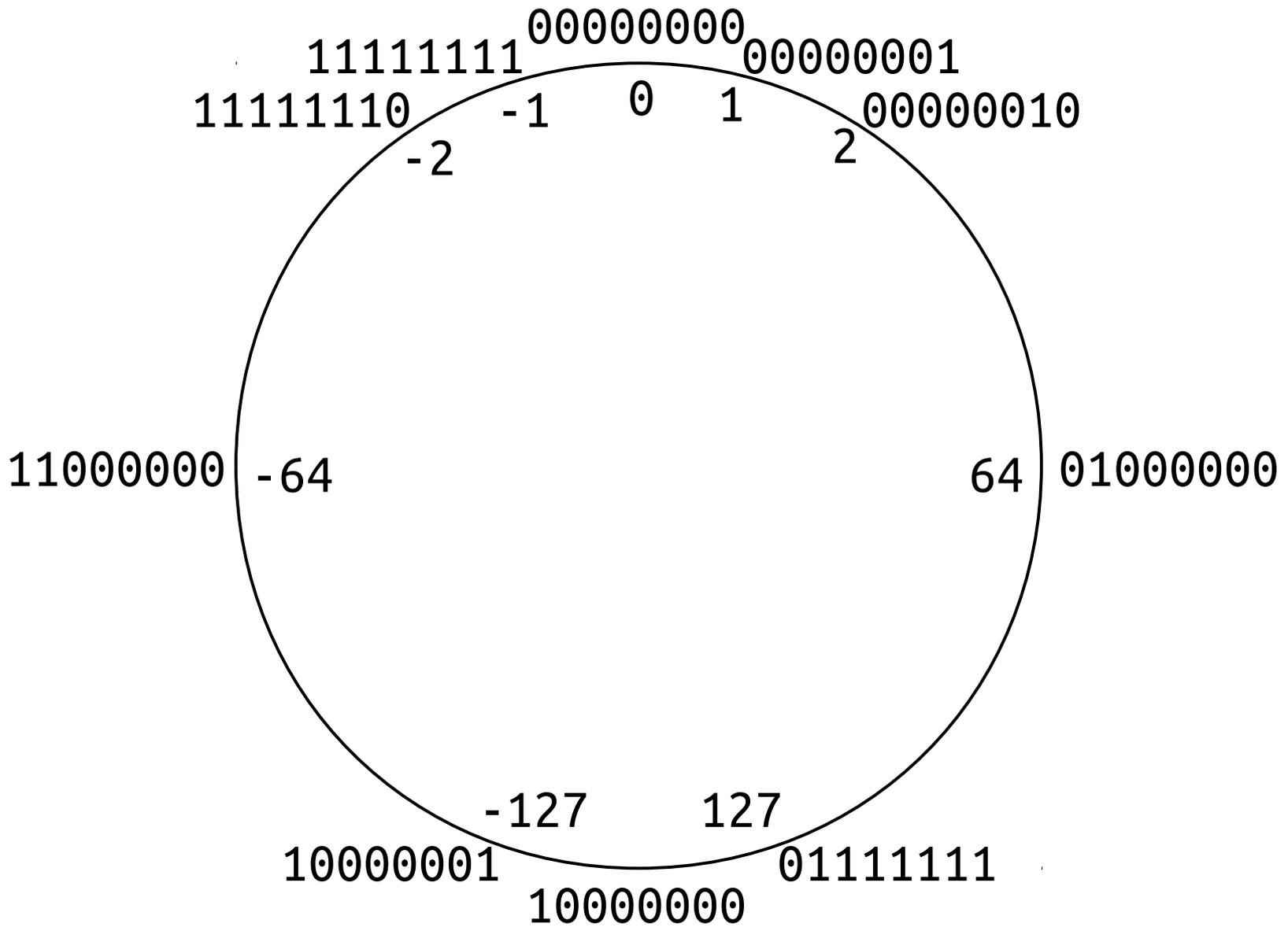
Works for all integer types, not just 1-byte

unsigned char Number Circle

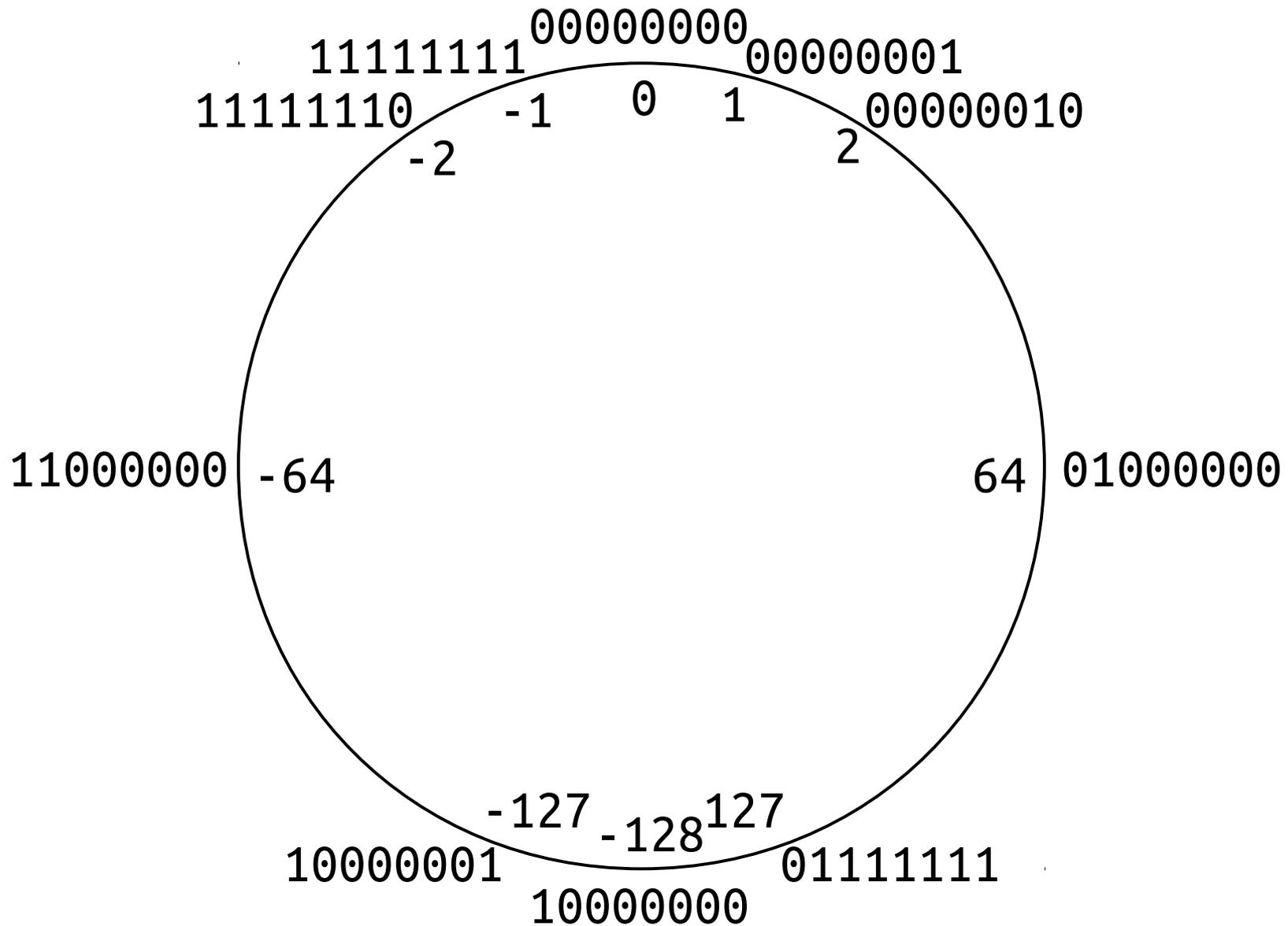




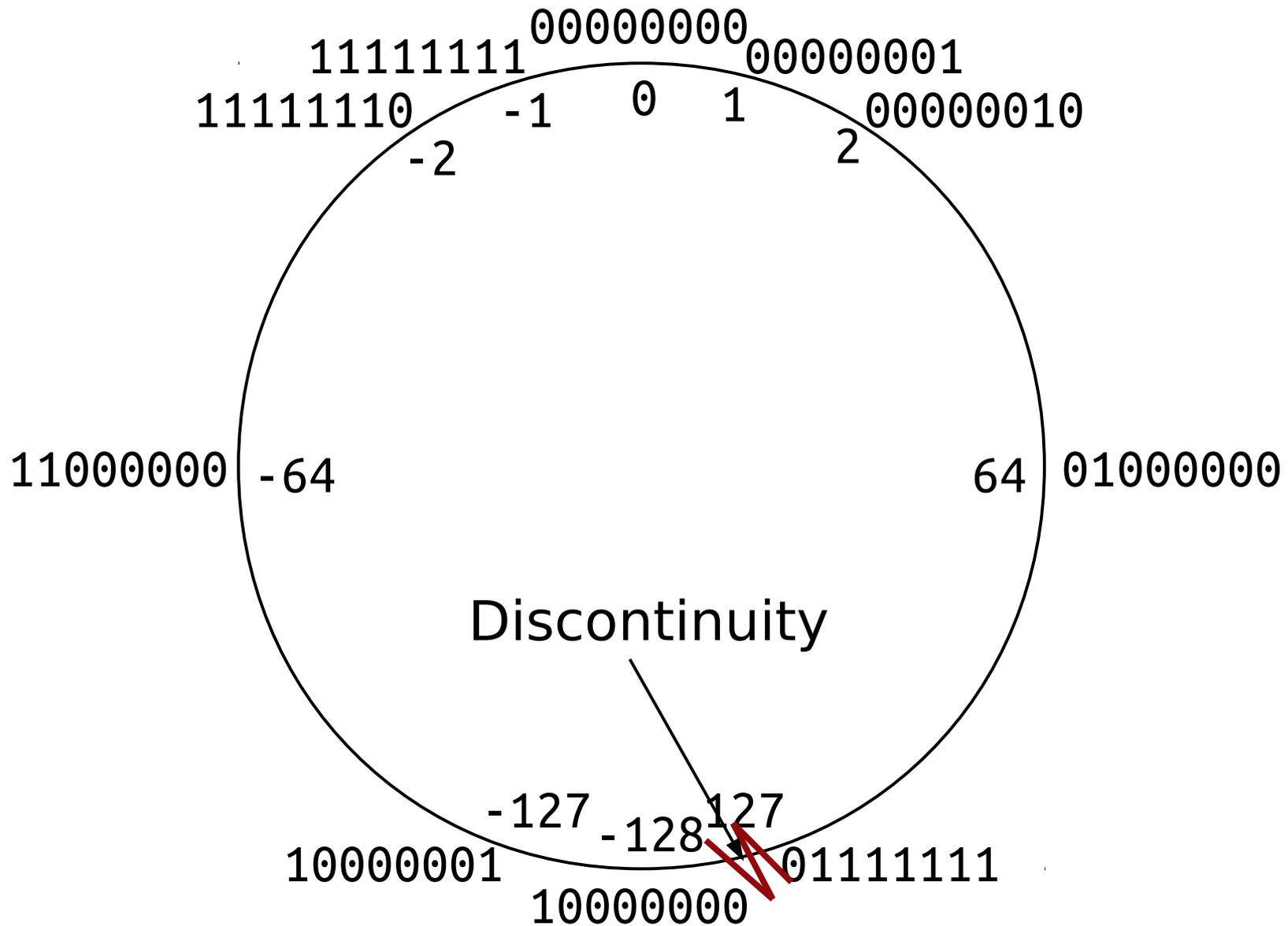




signed char Number Circle



signed char Number Circle



signed char

$$-n = \sim n + 1$$

Same 256 bit patterns

Half the bit patterns used for negatives

MSB = 1 means negative (sign bit)

Discontinuity

Different place than unsigned

Asymmetry

What is $-(-128)$?

(signed char) -(-128)

$$\begin{aligned} -(-128) &= -0x80 && -(1000\ 0000) \\ &= \sim 0x80 + 1 && \sim(1000\ 0000) + 1 \\ &= 0x7f + 1 && \quad 0111\ 1111 + 1 \\ &= 0x80 = -128 && \quad 1000\ 0000 \end{aligned}$$

-128 is its own arithmetic inverse

Larger Integer Types

C integer types

All but char default to signed (char's default is not defined)

Data type sizes

char: 1 byte, -128 to 127

short: 2 bytes, +/- 32k

int: 4 bytes, +/- 2 billion

long: 8 bytes, +/- [big number]

Code Examples

Signed vs. Unsigned

What's the same?

Most bitwise ops (&, |, ^, ~, <<)

Most arithmetic ops (+, -, *)

What's different?

Comparison (>, <)

Right shift: arithmetic vs .logical

Fill with sign bit vs. zero

Assignment to larger type (e.g. assign short to int)

Sign extend vs. zero extend

Preserves value

Conclusions

In the end, all operations manipulate bits

Assignment, parameter passing just copy bits around

Signed and unsigned are interpretations of bit patterns

Bits aren't fundamentally one or the other

Just like binary, decimal, hex are ways to read/write and think about bits

Summary

Introduction to computer arithmetic

Binary addition and subtraction

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Represent negative numbers

Compare/contrast unsigned and signed integers

This week's lab and assignment

Practice with bits, bitwise ops, ints

Next time: pointers and memory