CS 107
Lecture 24:
Optimization I

Wednesday, March 6, 2024

Computer Systems
Winter 2024
Stanford University
Computer Science Department

Reading: Course Reader: x86-64 Assembly Language, Textbook: Chapter 3.1-3.4

Lecturer: Chris Gregg
Today's Topics

• Programs from class: /afs/ir/class/cs107/samples/lect24
• Optimization:
  • What optimizing compilers do and don't do
  • GCC explorer: https://godbolt.org/g/3p91t2
• Memory Performance
  • How memory is organized
  • Caching
  • Impact of temporal and spatial locality
A few quotes on optimization

“We should forget about small efficiencies, say about 97% of the time: premature optimization is the root of all evil.”
— Donald Knuth

“More computing sins are committed in the name of efficiency (without necessarily achieving it) than for any other single reason - including blind stupidity.”
— W.A. Wulf (University of Virginia)

“Bottlenecks occur in surprising places, so don't try to second guess and put in a speed hack until you have proven that's where the bottleneck is.”
— Rob Pike (Google, created UTF-8, the Go programming language)
Measure, measure, measure! Time your code to see if there is even an issue. We optimize code to make it faster (or smaller) — if there isn't a problem already, don't optimize. In other words, if it works okay at the scale you care about, don't try and optimize.

For example, if the code scales well already, it probably doesn't need to be optimized further.

Use the correct algorithm and design — optimization won't change Big O or fix a bad design, and your biggest win will be because you've chosen the correct algorithms to begin with.
Optimization Considerations

Keep it simple! — Simple code that is easy to understand and debug is generally best. In this case you are optimizing the programmer's time. This is especially true for the parts of your code that aren't providing the bottleneck.

Let gcc do its optimizations — don't pre-optimize, and after you compile with a high optimization in gcc, look at the assembly code and analyze it to see where you may be able to optimize.

Optimize explicitly as a last resort — measure again, and attack the bottlenecks first.
Optimization Blockers

Programmers need to be careful to write code that can be optimized!

Although this isn't always possible, it is a good goal to have.

Let's look at two functions:

```c
void twiddle1(long *xp,
              long *yp)
{
    *xp += *yp;
    *xp += *yp;
}
```

```c
void twiddle2(long *xp,
              long *yp)
{
    *xp += 2 * *yp;
}
```

The functions perform the same thing, right?
Optimization Blockers

Oops — if the pointer is the same, we have a problem! Pointers can be optimization blockers. GCC won’t optimize twiddle1 to twiddle2, because it could lead to incorrect code.

```c
void twiddle1(long *xp, long *yp)
{
    *xp += *yp;
    *xp += *yp;
}

void twiddle2(long *xp, long *yp)
{
    *xp += 2 * *yp;
}
```

```
$ ./twiddle 2 3
a: 2, b: 3
after twiddle1(&a, &b), a = 8

a: 2, b:3
after twiddle2(&a, &b), a = 8

a: 2
after twiddle1(&a, &a), a = 8

a: 2
after twiddle2(&a, &a), a = 6
```
Optimization Examples: Constant Folding

GCC explorer: https://gcc.godbolt.org/z/vbfTjehPa

```c
unsigned long CF(unsigned long val)
{
    unsigned long ones = ~0U/UCHAR_MAX;
    unsigned long highs = ones << (CHAR_BIT - 1);
    return (val - ones) & highs;
}
```

- **-O0**

```assembly
pushq %rbp
movq %rsp, %rbp
movq %rdi, -24(%rbp)
movq $16843009, -8(%rbp)
movq -8(%rbp), %rax
salq $7, %rax
movq %rax, -16(%rbp)
movq -24(%rbp), %rax
subq -8(%rbp), %rax
andq -16(%rbp), %rax
popq %rbp
ret
```

- **-O2**

```assembly
leaq -16843009(%rdi), %rax
andl $2155905152, %eax
ret
```

The compiler doesn't need to do as many real-time calculations, and folds the constants into two calculations.
Optimization Ex: Common Subexpression Elimination

GCC explorer: https://gcc.godbolt.org/z/5oK5PxY5s

```c
int CSE(int num, int val)
{
    int a = (val + 50);
    int b = num * a - (50 + val);
    return (val + (100 / 2)) + b;
}
```

The compiler is able to eliminate subexpressions by determining that they are the same.

```assembly
pushq %rbp
movq %rsp, %rbp
movl %edi, -20(%rbp)
movl %esi, -24(%rbp)
movl -24(%rbp), %eax
addl $50, %eax
movl %eax, -4(%rbp)
movl -20(%rbp), %eax
imull -4(%rbp), %eax
movl -24(%rbp), %edx
addl $50, %edx
subl %edx, %eax
movl %eax, -8(%rbp)
movl -24(%rbp), %eax
leal 50(%rax), %edx
movl -8(%rbp), %eax
addl %edx, %eax
popq %rbp
ret
```
Optimization Ex: Strength Reduction

GCC explorer: https://gcc.godbolt.org/z/cW7n4G3hT

```c
int SR(int a, int val)
{
    unsigned int b = 5*val;
    int c = b / (1 << val);
    return (b + c) % 2;
}
```

```
pushq %rbp
movq %rsp, %rbp
movl %edi, -20(%rbp)
movl -20(%rbp), %edx
movl %edx, %eax
sal $2, %eax
addl %edx, %eax
movl %eax, -4(%rbp)
movl -20(%rbp), %eax
movl -4(%rbp), %edx
movl %eax, %ecx
shrl %cl, %edx
movl %edx, %eax
movl %eax, -8(%rbp)
movl -8(%rbp), %edx
movl -4(%rbp), %eax
addl %edx, %eax
andl $1, %eax
popq %rbp
ret
```

```
leal (%rdi,%rdi,4), %eax
movl %edi, %ecx
movl %eax, %edx
shrl %cl, %edx
addl %edx, %eax
andl $1, %eax
ret
```

The compiler replaces expensive (strong) operations (e.g., divides) with equivalent expressions that are less strong.
Optimization Ex: Code Motion

GCC explorer: https://gcc.godbolt.org/z/zh6YMjjb8

```c
int CM(int val)
{
    int sum = 0;
    do {
        sum += 6 + 14 * val;
    } while (sum < (9 / val));
    return sum;
}
```

```
-O0
```

```
pushq %rbp
movq %rsp, %rbp
movl %edi, -20(%rbp)
movl $0, -4(%rbp)
.L2:
    movl -20(%rbp), %eax
    addl %eax, %eax
    leal 0(%rax,8), %edx
    subl %eax, %edx
    movl %edx, %eax
    addl $6, %eax
    addl %eax, -4(%rbp)
    movl $9, %eax
    cltd
    idivl -20(%rbp)
    cmpl -4(%rbp), %eax
    jg .L2
    movl -4(%rbp), %eax
popq %rbp
ret
```

```
-O2
```

```
movl $9, %eax
xorl %ecx, %ecx
cltd
idivl %edi
imull $14, %edi, %esi
addl $6, %esi
.L2:
    addl %esi, %ecx
    cmpl %eax, %ecx
    jl .L2
    movl %ecx, %eax
ret
```

The compiler moves code out of loops if it can: it only needs to perform the operation once, so it does.
Optimization Ex: Dead Code Elimination

GCC explorer:
https://gcc.godbolt.org/z/r1eeWEarG

```c
int DC(int param1, int param2)
{
    if (param1 < param2 && param1 > param2) // can this test ever be true?
        printf("The end of the world is near!\n");
    int result;
    for (int i = 0; i < 1000; i++)
        result *= i;
    if (param1 == param2) // if/else obviously same on both paths
        param1++;
    else
        param1++;
    if (param1 == 0) // if/else no-so-obviously same on both paths
        return 0;
    else
        return param1;
}
```

The compiler realizes that most of the code does not perform useful work, so it just removes it!
gcc and optimization

- **-O0**: // faithful, literal match to C, and the best for debugging (but everything goes on the stack)
- **-Og**: // streamlined, but debug-friendly (we used this for most assignments and bank vault)
- **-O2**: // apply all acceptable optimizations
- **-O3**: // even more optimizations, but relies strongly on exact C specification (e.g., if you assume, for instance that signed numbers wrap, your code might break with this optimization level)
- **-Os**: // optimize for code size; performs the -O2 optimizations that don't increase the code size (e.g., no function alignment)

You can see all optimizations that will be run by compiling with the following flags:

```
gcc -O3 prog.c -o prog -Q --help=optimizers
```
gcc and optimization

gcc knows the hardware you are running on, including:

• Register allocation
• Instruction choice
• Alignment

All transformations made by gcc during optimization should be legal and equivalent to your original C program.

• The compiler knows about compile time, not run time.
• The optimizations are conservative (e.g., it rarely tries to perform too much optimization with pointers, and it rarely removes a function unless it knows enough about it to do so).
How do we measure performance?

- Timers! There are a number of different ways to do it. One timer, `rtdsc`, is only available in assembly, although we can write a C program to access it by using "inline assembly" or linking to an assembly function.
- We can also use `valgrind --tool=callgrind`.

You should time unoptimized vs. optimized:

- `mult.c`
- `sorts.c`
- `fact.c`
- `array.c`
gcc cannot fix algorithmic weaknesses, or big-O!

If you optimize too early yourself, gcc may not be able to figure out any further optimizations.

gcc generally cannot remove function calls, nor can it "see through" pointers to determine if aliasing has occurred.
Example: summing the char values in a string

```c
int charsum(char *s)
{
    int sum = 0;
    for (size_t i = 0; i < strlen(s); i++) {
        sum += s[i];
    }
    return sum;
}
```

1. What is going to cause the bottleneck for this function?
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   - `strlen(s)` -- it must search one character at a time!
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2. What can the compiler do about it?
What gcc can do

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}
```

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   strlen(s) -- it must search one character at a time!

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```c
int charsum2(char *s)
{
    int sum = 0;
    size_t len = strlen(s);
    for (size_t i = 0; i < len; i++) {
        sum += s[i];
    }
    return sum;
}
```
What gcc cannot do

Example: converting a string to lowercase

```c
void lower1(char *s)
{
    for (size_t i = 0; i < strlen(s); i++) {
        if (s[i] >= 'A' && s[i] <= 'Z') {
            s[i] -= ('A' - 'a');
        }
    }
}
```

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```

1. What is going to cause the bottleneck for this function?
   - `strlen(s)` -- it must search one character at a time!

2. Can the compiler move this out of the loop?
What gcc cannot do

Example: converting a string to lowercase

```c
void lower1(char *s)
{
    for (size_t i = 0; i < strlen(s); i++) {
        if (s[i] >= 'A' && s[i] <= 'Z') {
            s[i] -= ('A' - 'a');
        }
    }
}
```

1. What is going to cause the bottleneck for this function?
   - `strlen(s)` -- it must search one character at a time!

2. Can the compiler move this out of the loop?
   - It cannot! Because `s` is changing, the compiler won't risk moving `strlen()` outside the loop. It can't figure out that a zero won't ever be put into the string (changing the string's length).