Course Information

Instructor:	Pascal Stang 052 Packard Building (EE281 lab) Telephone: Home: 408-244-7737 Email: pstang@stanford.edu Office hours: Wed/Fri 1:15-2:15, and by appointment
Guest Lecturer:	John Gill 266 Packard Building Telephone: 650-723-4715, Fax: 650-723-8473 Email: gill@isl.stanford.edu
Teaching Assistant:	David Black-Schaffer 052 Packard Building (EE 281 lab) Telephone: TBD Email: davidbbs@stanford.edu Office hours: Mon/Wed 7-10pm, and by appointment
Secretary:	Denise Cuevas 267 Packard Building Telephone: 650-723-4731. Email: denise@isl.stanford.edu
Laboratory:	052 Packard Building. Telephone: 650-725-1768. Lab is accessible to EE281 students 24 hours a day by card key.
Lectures:	Mitchell B67, Wed/Fri 11:00am - 12:15pm
Required Text:	Atmel AVR databook (provided in class). Other readings may be posted on website or handed out in class.
Required Materials:	Atmel STK200/500 AVR Microcontroller Starter Kit. Starter kits will be issued to project teams in the second week of class.
Prerequisites:	EE 121 or EE 183 or equivalent digital logic design. CS 110 or EE 182 or equivalent assembly language programming.
Grading:	40% Laboratory assignments 17% In-class presentation 40% Final project 3% Professor's discretion
Late Policy:	Because each laboratory assignment builds upon the previous, timely completion is vital. Late assignments will be penalized 20% per day unless prior arrangements are made. Everyone gets 3 free late days. Weekends count as one late day.
Online Resources:	Class webpage: http://www.stanford.edu/class/ee281 Class mailing list: send email to majordomo@lists.stanford.edu with "subscribe ee281" in the body (subject line must be blank). Class newsgroup: su.class.ee281. This newsgroup will archive the class mailing list.