Load-Time Relocation

- Operating System
- Process 3
- Process 6
- Process 1
Process Virtual Address Space

Base & Bounds Example

Process Virtual Address Space

2000
1420
140
62
0

RETURN

CALL 140

SP
64-bit Virtual Address

PML4 (Page Directory Pointer Table)

PML3 (Page Directory)

PML2 (Page Table)

PML1 (Page Table)

Page Frame

Offset

52-bit Physical Address
write(fd, buffer, length)
OS and User in Same Address Space

- Operating System
- Stack
- Data
- Code

- OS in all Address Spaces
- Private Data/Stack
- Shared Code

Process 1

Process 2